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⑦① Applicant : **MITSUBISHI DENKI KABUSHIKI  
KAISHA**  
2-3, Marunouchi 2-chome Chiyoda-ku  
Tokyo (JP)

⑦② Inventor : Ido, Kihei, c/o Mitsubishi Denki K.K.  
Denshi-shohinkaihatu Kenkyusho, 1,  
Babazusho  
Nagaokakyo-shi, Kyoto 617 (JP)  
Inventor : Yamada, Masako, c/o Mitsubishi  
Denki K.K.  
Denshi-shohinkaihatu Kenkyusho, 1,  
Babazusho  
Nagaokakyo-shi, Kyoto 617 (JP)  
Inventor : Kosaka, Hideaki, c/o Mitsubishi  
Denki K.K.  
Denshi-shohinkaihatu Kenkyusho, 1,  
Babazusho  
Nagaokakyo-shi, Kyoto 617 (JP)  
Inventor : Ohta, Masayuki, c/o Mitsubishi  
Denki Eng. K.K.  
Kyoto Jigyosho, 1, Babazusho  
Nagaokakyo-shi, Kyoto 617 (JP)

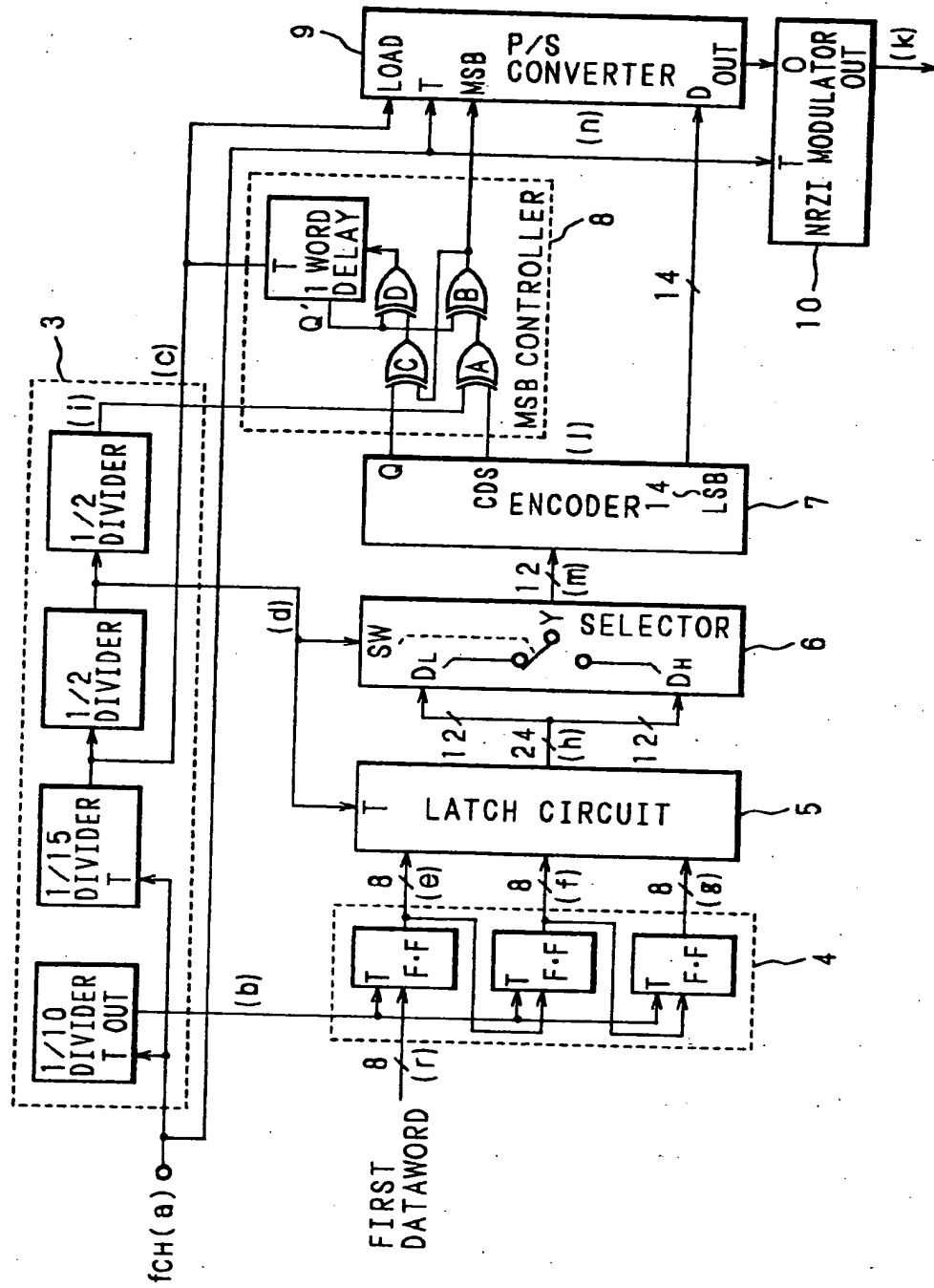
⑦④ Representative : **Burke, Steven David et al**  
R.G.C. Jenkins & Co. 26 Caxton Street  
London SW1H 0RJ (GB)

⑤④ Data conversion method and recording/reproducing apparatus using the same.

⑤⑦ A data conversion method, wherein a sequence of first  $r$ -bit datawords is divided into groups of  $x$  bits where  $x$  is the least common multiple of  $r$  and  $m$ , an arbitrary first dataword selected from  $x/r$  groups of first datawords is divided into  $x/m$ , an  $m$ -bit second dataword is formed by appending  $r/(x/m)$ -bit data, obtained by dividing the first dataword into  $x/m$ , to the LSB or MSB side of one or other of the non-divided first datawords, and the word-converted  $m$ -bit second dataword is converted to an  $n$ -bit codeword ( $m < n$ ).

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Fig. 27



## BACKGROUND OF THE INVENTION

### Field of the Invention

The present invention relates to a data conversion method for converting digital data to signals suitable for the recording system or the transmission channel used when recording or transmitting the digital data onto magnetic tape, and a recording/reproducing apparatus employing the data conversion method.

### Description of Related Art

Prior art data conversion methods employed in magnetic recording/reproducing apparatus include, for example, an 8/10 modulation method such as disclosed in "THE DAT CONFERENCE STANDARD" (issued June 1987). The 8/10 modulation method is a data conversion method in which digital data are partitioned into datawords of 8 bits each for conversion into 10-bit codewords. Fig. 1 is a circuit block diagram for explaining this data conversion method, and Fig. 2 is a data conversion table used for the same. In Fig. 1, the reference numeral 1 designates an encoder for accepting eight-bit digital data and a one-bit table select signal ( $Q'$ ) at its respective inputs and for outputting a total of 11 bits, i.e. a 10-bit codeword plus a one-bit signal ( $Q$ ) for selecting the table for the next codeword. Further, the numeral 2 denotes a flip-flop for delaying the codeword table select signal by one dataword. The encoder 1 includes a read-only memory (ROM) or the like which contains the data conversion table shown in Fig. 2, wherein codewords of CDS (Codeword Digital Sum) = 0 are mapped on a one-to-one basis to 256 datawords from "00" to "FF" of hexadecimal numeral, while in the case of codewords of CDS  $\neq 0$ , pairs of codewords, one with CDS = +2 and the other with CDS = -2, are each mapped to one dataword, the table of  $Q' = -1$  consisting of codewords of CDS = +2 and the table of  $Q' = +1$  consisting of codewords of CDS = -2. The table select signal ( $Q$ ) is used to select the CDS (the table) having the direction that suppresses the dispersion of charges in the codeword sequence.

The operation of the above circuit will now be explained with reference to the timing diagram of Fig. 3. In Fig. 3, the reference signs (a), ( $Q$ ), and (b) correspond to inputs/outputs at the respective parts shown in Fig. 1, and the reference signs (c) and (d) respectively represent an output signal from an NRZI converter (not shown) and a DSV (Digital Sum Variation) value at the end of codeword.

First, an eight-bit dataword "FF" is input to the encoder 1, along with the table select signal ( $Q'$ ) = -1, and consequently, the encoder 1 outputs a 10-bit codeword "1111101010" of CDS = +2 corresponding to "FF" for  $Q' = -1$ . At the same time, the table select sig-

nal  $Q = -1$  is output for the next codeword. The parallel 10-bit signal is then converted to a serial signal, after which the signal is NRZI-modulated. As a result, the DSV value at the end of the codeword becomes +2.

Next, when "00" is input to the encoder 1, the encoder 1 outputs  $Q = 1$  together with a 10-bit signal "0101010101" of CDS = 0 corresponding to "00" for  $Q' = -1$  which is produced by introducing a one-symbol delay in the previous output  $Q = -1$ . As a result, the DSV value at the end of the codeword after NRZI modulation remains at +2.

Next, when "11" is input to the encoder 1, the encoder 1 outputs  $Q = -1$  together with a 10-bit signal of CDS = -2 corresponding to "11" for  $Q' = 1$ . As a result, the DSV value at the end of the codeword after NRZI modulation becomes zero. In this manner, for each eight-bit dataword input to the encoder 1, a codeword to be output is selected from the table of either  $Q' = -1$  or  $Q' = 1$  corresponding to the dataword on the basis of the table select signal output previously. The DSV at the end of each codeword after NRZI modulation can only take the value 0, +2 or -2. This means that the DSV dispersion is suppressed, as a result of which DC-free data conversion is realized.

As described above, according to the prior art data conversion method, eight-bit data is converted to a 10-bit codeword of CDS = 0 or CDS = +2 or -2, and a DC-free signal is produced with the DSV dispersion suppressed, thereby minimizing intersymbol interference on the transmission channel and thus increasing the recording density per track. However, for recent digital magnetic recording/reproducing apparatus using a rotary head, a recording density as high as several square micrometers per bit is demanded, which necessitates not only increasing the recording density per track but also reducing the track width down to several micrometers. To implement such apparatus, it is essential to employ a dynamic tracking following (DTF) control system whereby pilot signals for tracking are recorded on the main track recorded by the rotary head and the playback head is controlled to follow the recorded track curves during playback. When the prior art data conversion method is employed in such apparatus for multiplex recording of the pilot signals, the digital signal spectral distribution has to be obtained down to ultra low frequency ranges although the recorded information signals contain no DC components; the resulting problem is that the pilot signals cause external disturbances, leading to increased errors in the detection of the digital signals.

One possible approach to overcoming the problem of the pilot signals causing external disturbances to the digital signals may be by generating pilot signals synchronized to the digital signals. However, the prior art data conversion method is effective only in suppressing the DSV dispersion and is not capable of actively controlling the DSV, and therefore, has the problem that it cannot generate pilot signals syn-

chronized to the digital signals.

Fig. 4 shows a DAT recording format employed in a magnetic recording apparatus using the 8/10 modulation method. As shown, according to the format of Fig. 4, ATF areas for tracking control are provided in each of which a pilot signal for tracking control is recorded. Further, Fig. 5 shows a digital VTR recording format which is disclosed in Japanese Patent Application Laid Open No.3-217179 (1991). As shown, the track is divided into a video data area, an audio data area, a servo pilot area, and a sub code area, the pilot signal being recorded in the servo pilot area only.

According to the above construction of the prior art, it is not possible to control the DSV in such a manner as desired, and a separate area has to be reserved for recording a pilot signal for tracking control. Accordingly, accurate tracking control cannot be realized without increasing the data amount and hence increasing the recording rate, which makes it difficult to achieve high density recording.

#### SUMMARY OF THE INVENTION

It is an object of the present invention to provide a data conversion method which, by suppressing low frequency components, can minimize intersymbol interference on the transmission channel, thus permitting increased per-track recording density, as in the prior art data conversion method, and which is capable of generating pilot signals synchronized to digital signals, which has not been possible with the prior art method, and thus achieves increased recording density with reduced track width.

It is another object of the present invention to provide a recording/reproducing apparatus optimized for the data conversion method capable of generating pilot signals synchronized to digital signals.

It is a further object of the present invention to provide a data conversion method which is capable of generating pilot signals for tracking control and which involves hardly any increase in the recording rate and therefore permits high density recording, and a recording/reproducing apparatus using such a data conversion method.

According to the present invention, there is provided a data conversion method for word-converting an  $r$ -bit first dataword to an  $m$ -bit second dataword ( $r < m$ ) and converting the word-converted  $m$ -bit second dataword to an  $n$ -bit codeword ( $m < n$ ), in which, for  $r/m$  word-conversion, a sequence of first datawords is divided into groups of  $x$  bits where  $x$  is the least common multiple of  $r$  and  $m$ , an arbitrary first dataword selected from  $x/r$  groups of first datawords is divided into  $x/m$ , and  $r/(x/m)$ -bit data obtained by dividing the first dataword into  $x/m$  is appended to the LSB (or MSB) side of one or other of the non-divided first datawords to form the  $m$ -bit second dataword. The  $m$ -bit second dataword can thus be handled as  $r$

+  $(r/(x/m))$  (or  $(r/(x/m)) + r$ ). Therefore, if, in  $m/n$  conversion, the  $n$ -bit codeword is formed by dividing it into  $n_1$  and  $n_2$  bits, the data conversion can be performed by relating  $r$  to  $n_1$  and  $r/(x/m)$  to  $n_2$ . This serves to reduce the possibility of error propagation due to a bit error that may occur in reverse data conversion.

Furthermore, when converting the word-converted  $m$ -bit second dataword to the  $n$ -bit codeword, the number of successive 0s between a bit "1" and the next bit "1" in each  $n$ -bit codeword is limited to 4, and two codewords, one with CDS = +1 and the other with CDS = -1, are paired and related to the  $m$ -bit second dataword, the two codewords being selectively used in accordance with a DSV control signal. This enables the DSV to be controlled at a desired value for each codeword, thereby achieving spectrum suppression in a relatively low frequency range. Also, by controlling the CDS polarity in accordance with the DSV control signal, a pilot signal of the DSV variation cycle synchronized to digital data can be generated in the low frequency band where the digital data power spectrum exhibits an abrupt drop.

When the above data conversion method is employed in a recording/reproducing apparatus, the number of first datawords to be recorded in a data block where an error-correcting code and an error-detection code are appended for every synchronizing signal is set at an integral multiple of  $x/r$ . The recording/reproducing apparatus thus constructed achieves an efficient code format that does not require redundant bits.

The recording/reproducing apparatus employing the above data conversion method has: decoding means for decoding  $n_1$  bits in the reproduced  $n$ -bit codeword into  $r$  bits, the reproduced  $n$ -bit codeword being divided into  $n_1$  bits and  $n_2$  bits for reverse conversion into the  $m$ -bit second dataword; decoding means for decoding the  $n_2$  bits into  $r/(x/m)$  bits; decoding means for decoding the  $n$  bits into the  $m$  bits; Identifying means for identifying the type of bits at prescribed positions in the  $n$ -bit codeword and for outputting an identification signal designating the identified type; and means for selecting decoded data from the respective decoding means on the basis of the identification signal supplied from the identifying means and for outputting the decoded second dataword. This construction serves to reduce the possibility of the error propagation that may occur between decoded first datawords due to a single bit random error in the  $n$ -bit codeword.

Another recording/reproducing apparatus of the invention has: means for recording multiple kinds of data in partitioned areas; means for relating 14-bit codewords of CDS = 0 and pairs of codewords of CDS =  $\pm 2$  to respective 12-bit datawords when encoding and recording at least one of the multiple kinds of data and for encoding the data by selectively using these

codewords; and means for appending one bit to each 14-bit codeword to form a pair of codewords, one with  $CDS = +1$  and the other with  $CDS = -1$ , when encoding and recording at least one other of the multiple kinds of data and for encoding the data by selectively using these codewords.

In the above recording/reproducing apparatus, when encoding and recording at least one of the area-partitioned multiple kinds of data, either a 14-bit codeword of  $CDS = 0$  or a pair of codewords differing only in MSB, one with  $CDS = +2$  and the other with  $CDS = -2$ , are related to one dataword, and the dataword is encoded by selectively using these codewords, thus constructing a DC-free code of  $T_{min} = 0.86T$  and  $T_{max} = 4.29T$ ; on the other hand, when encoding and recording at least one other of the multiple kinds of data, one bit is appended to each 14-bit codeword to form a pair of codewords, one with  $CDS = +1$  and the other with  $CDS = -1$ , and the data is encoded by selectively using these codewords, thus constructing a code that provides the DSV coming round to the same value at prescribed intervals.

The above and further objects and features of the invention will more fully be apparent from the following detailed description with accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a diagram showing the circuit configuration of a prior art data converting apparatus.

Fig. 2 is a code conversion table according to a prior art data conversion method.

Fig. 3 is a diagram for explaining the operation of the data converting apparatus of Fig. 1.

Fig. 4 is a diagram showing a recording format of a prior art DAT.

Fig. 5 is a diagram showing a recording format of a prior art digital VTR.

Figs. 6 is a diagram showing the number of codewords for deriving codewords in accordance with a first embodiment of the invention.

Figs. 7 is a diagram showing the number of codewords for deriving codewords in accordance with a first embodiment of the invention.

Figs. 8 is a diagram for explaining the construction of code conversion tables according to the first embodiment of the invention.

Figs. 9 is a diagram for explaining the construction of code conversion tables according to the first embodiment of the invention.

Figs. 10 is a diagram for explaining the construction of code conversion tables according to the first embodiment of the invention.

Figs. 11 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 12 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 13 is a diagram showing code conversion

according to the first embodiment of the invention.

Figs. 14 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 15 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 16 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 17 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 18 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 19 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 20 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 21 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 22 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 23 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 24 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 25 is a diagram showing code conversion according to the first embodiment of the invention.

Figs. 26 is a diagram showing code conversion according to the first embodiment of the invention.

Fig. 27 is a diagram showing the circuit configuration of a data converting apparatus for implementing the code conversion method of the first embodiment.

Fig. 28 is a diagram for explaining the operation of the data converting apparatus of Fig. 27.

Fig. 29 is a power spectrum diagram showing the effect of the first embodiment.

Fig. 30 is a diagram showing the structure of a first dataword block recorded by a recording/reproducing apparatus employing the data conversion method of the first embodiment.

Fig. 31 is a diagram showing the structure of the first dataword block recorded by the recording/reproducing apparatus employing the data conversion method of the first embodiment along with the structure of the first datawords recorded at the top of the block.

Fig. 32 is a diagram showing the configuration of a circuit for implementing a decoding method in the recording/reproducing apparatus employing the data conversion method of the first embodiment.

Fig. 33 is a diagram showing classifications for 5-bit LSB codewords in the codewords of the first embodiment.

Figs. 34 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 35 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 36 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 37 is a diagram showing code conversion

according to a second embodiment of the invention.

Figs. 38 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 39 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 40 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 41 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 42 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 43 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 44 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 45 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 46 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 47 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 48 is a diagram showing code conversion according to a second embodiment of the invention.

Figs. 49 is a diagram showing code conversion according to a second embodiment of the invention.

Fig. 50 is a diagram showing the circuit configuration of a data converting apparatus for implementing the data conversion method of the second embodiment.

Fig. 51 is a diagram showing a code select table according to the second embodiment.

Fig. 52 is a diagram for explaining the operation of the data converting apparatus of Fig. 50.

Fig. 53 is a diagram showing a recording format of a recording/reproducing apparatus according to the second embodiment.

## DESCRIPTION OF THE PREFERRED EMBODIMENTS

### (Embodiment 1)

A first embodiment of the invention will now be described below with reference to accompanying drawings. Now suppose a first dataword length  $r = 8$ , a word-converted second dataword length  $m = 12$ , and a data-converted codeword length  $n = 15$ , to form a code with a modulation parameter  $T_{\max}/T_{\min} = 5$ . At this time,  $d = 0$  and  $k = 4$ , where  $d$  is the minimum number of 0s between an arbitrary 1 and the next 1, and  $k$  is the maximum number of 0s between an arbitrary 1 and the next 1. The NRZI (F) rule is used to form the code. To achieve such a data conversion, the maximum number of successive 0s in each codeword is limited to 3 on the MSB side, 1 on the LSB side, and 4 within codeword. In this situation, the number of possible codewords having the MSB of 0 and satisfying the 0 run length condition is given in

Fig. 6 for each CDS.

To form a DC-free code,  $2^{12}$  pairs (4096 pairs) of codewords, each pair having codewords of different CDS polarities, should be provided. The numbers given in Fig. 6 are only for codewords whose MSB is 0; by converting the MSB to 1, codewords of reverse CDS polarity can be obtained while satisfying the 0 run length condition. Accordingly, of the codewords given in Fig. 6, only the codewords of  $CDS = \pm 1$  are enough to satisfy the minimum required number of second datawords  $= 2^{12}$  ( $4096 < \text{number of codewords} = 4650$ ). Therefore, by using only the codewords of MSB = 0 and  $CDS = \pm 1$  and by setting the MSB to 0 or 1, it is possible to suppress the dispersion of DSV.

Fig. 7 shows possible combinations of codewords  $n_1$  and  $n_2$  when the codewords of  $CDS = \pm 1$  given in Fig. 6 are each divided into  $n_1 = 10$  bits and  $n_2 = 5$  bits,  $n_1$  representing the 10 bits on the MSB side and  $n_2$  representing the 5 bits on the LSB side. In Fig. 7, Group A consists of  $n_1$  codewords of  $CDS = 0$ , Group B of  $n_1$  codewords of  $CDS = +2$ , Group C of  $n_1$  codewords of  $CDS = -2$ , Group D of  $n_1$  codewords of  $+4$ , and Group E of  $n_1$  codewords of  $CDS = -4$ . Each of the codeword groups A to E is subdivided in accordance with the 0 run length at codeword end resulting from the concatenation of the codewords  $n_1$  and  $n_2$ .

First, we focus our attention on Group A. It can be seen that there are 18 different  $n_2$  codewords that can be paired with A1 while, of the 18 codewords, 17 codewords excluding the codeword "02" can also be paired with A2. Therefore, for Group A, 16 codewords, excluding the codewords "02" and "05", are used, and  $m = 12$  bits are divided into  $m_1 = 8$  bits and  $m_2 = 4$  bits at the time of  $m/n$  conversion, to realize  $m_1/n_1$  ( $8/10$ ) conversion and  $m_2/n_2$  ( $4/5$ ) conversion, respectively. This coding technique serves to avoid propagation of errors between divided codewords at the time of decoding. To utilize this property, when 8-bit first datawords of length ( $r$ ) supplied from an error-correcting circuit are word-converted to 12-bit second datawords of length ( $m$ ), four bits separated from the eight bits are mapped to  $m_2$ , while non-divided 8 bits are mapped to  $m_1$ . As a result, when a random error occurred to one bit in  $n$  bits during the reconstruction process, the error occurring to the first dataword after decoding is limited only to one dataword; the error is thus prevented from propagating between datawords.

In the first embodiment, 1s and 0s used to represent one-bit signals are binary numbers, a 1 representing a high level and a 0 a low level. On the other hand, "0" to "F" used to represent datawords, codewords, or parallel data bit sequences are hexadecimal numbers.

If the above coding method is provided in 256 pairs, a single bit error in  $n$  bits during the reconstruction process can be prevented from propagating be-

tween first datawords after decoding. However, as is apparent from Fig. 7, under the condition that satisfies the modulation parameter of the data conversion method of the first embodiment, the above coding method can be applied only to Group A, and cannot be applied to the other Groups B to E.

In view of the above situation, we now consider a method of coding, as shown in Figs. 8 to 10 wherein the  $m1/n1$ ,  $m2/n2$  coding method is divided into three major coding groups, i.e. the first coding group consisting only of Group A codewords corresponding to the first datawords  $m1 = "00"$  to  $"73"$ , the second coding group consisting of Group B and Group C codewords corresponding to the first datawords  $m1 = "74"$  to  $"BA"$ , and the third coding group consisting of the codewords in the other groups as well as the remaining codewords in Group B and Group C corresponding to the first datawords  $m1 = "BB"$  to  $"FF"$ .

First, referring to the first coding group of Fig. 8 which consists only of Group A codewords, if an error occurred to one bit in  $n$  bits in the reconstruction process, the error occurring to the first dataword after decoding is limited only to one dataword and is thus prevented from propagating between datawords.

Next, in the second coding group shown in Fig. 9, there is provided a one-to-one correspondence for the  $m2/n2$  conversion, but for the  $m1/n1$  conversion, two  $n1$  codewords are mapped to one  $m1$ . Therefore, of the encoded 15 bits, if the 10 bits mapped to  $n1$  contains a single bit error, the error occurring to the first dataword after decoding is limited only to one dataword and is thus prevented from propagating between datawords. However, if there is an error in one bit out of the five bits mapped to  $n2$ , error propagation can occur between first datawords after decoding from the probability point of view.

Further, in the third encoding group shown in Fig. 10, one  $m1$  is mapped to a plurality of  $n1$  codewords for the  $m1/n1$  conversion, while for the  $m2/n2$  conversion, a plurality of  $m2$  codewords are mapped to one  $n2$ . Therefore, any one bit error can cause error propagation between first datawords after decoding from the probability point of view whether the error is in  $n1$  or  $n2$ .

In the data conversion method wherein an 8-bit first dataword is word-converted to a 12-bit second dataword which is further converted to a 15-bit codeword, the above coding method has the effect of reducing the possibility of error propagation that may occur between first datawords after decoding due to a single bit detection error in the encoded 15 bits.

Code conversion tables thus constructed are shown in Figs. 11 to 26. The numbers given in Figs. 11 to 26 represent binary digital signals in hexadecimal notation; "0" to "F" in the uppermost row each correspond to the four bits on the LSB side of a 12-bit input codeword, and "00" to "FF" in the leftmost column each correspond to the eight bits on the MSB

side of a 12-bit input codeword, each row and column intersection "XXXX" forming the resulting 16-bit codeword. For example, when a 12-bit dataword "15A" is input, a codeword "9539" is obtained from the intersection between the row of "15" and the column of "A" (see Fig. 12). For a 12-bit input codeword (the second codeword), the resulting codeword consists of 16 bits, of which the MSB corresponds to a Q signal ("1" for a high level and "0" for a low level, representing the end level of an NRZI-modulated codeword when the MSB of the premodulation codeword is "0"), the 15th bit represents the CDS information ("1" for +1 and "0" for -1), and the remaining bits from the 14th bit to the LSB of the 15-bit codeword to be NRZI-modulated. For the  $m/n$  (12/15) data conversion, the codeword output is selected as 16 bits because the codeword MSB control is performed by comparing the CDS information of the codeword to be converted with the end level of the previous NRZI-modulated codeword on the basis of a DSV control signal of 50% duty cycle derived by further dividing the data conversion rate signal.

Fig. 27 is a diagram showing an example of a circuit configuration implementing the first embodiment. The reference numeral 3 designates a clock generator circuit which generates, from a channel clock for transmitting a data-converted code, a symbol clock of  $f_{CH}/10$  for transmitting a first codeword ( $r$ ), a clock ( $f_{MW}/2$ ) of  $f_{CH}/30$  (a value obtained by multiplying 24, the least common multiple of  $r$  and  $m$ , by  $n/m$  (10/8)) for word-converting the first dataword to the second dataword, a clock ( $f_{MW}$ ) for parallel-transmitting a converted  $n$ -bit codeword, and a DSV control signal ( $l$ ) for determining the variation frequency of DSV.

The numeral 4 is a shift register constructed from three stages of flip-flops (F-F) for transmitting 8-bit first datawords in parallel at the symbol clock ( $f_{sym}$ ); 5 is a latch circuit that latches at the clock ( $f_{MW}/2$ ) the 24-bit parallel signal output from the shift register 4; 6 is a selector for word-converting the first datawords of 3 bytes to two second datawords by using the clock ( $f_{MW}/2$ ) as a select switch; 7 is an encoder for data-converting each 12-bit dataword to a codeword selected from the tables shown in Figs. 11 to 26; and 8 is an MSB controller for outputting the MSB of the codeword in accordance with the Q and CDS information supplied from the encoder 7 and the DSV control signal ( $l$ ) supplied from the clock generator circuit 3, the MSB controller 8 having four EXOR circuits, A to D, and a one-word decay for delaying the end level of the previous NRZI-modulated codeword by one encoding cycle by using the clock ( $f_{MW}$ ). Further, the numeral 9 designates a parallel/serial converter for loading the encoded 15-bit parallel signal at the clock ( $f_{MW}$ ) and for converting the parallel signal to a serial signal which is transmitted at the channel clock ( $f_{CH}$ ),

and the numeral 10 indicates an NRZI modulator for causing state inversion (high to low and low to high transitions) when signal "1" is input.

Fig. 28 is a timing diagram for explaining the operation of the circuit shown in Fig. 27. The reference signs (a) to (k), (m), (n), and (r) correspond to the respective points designated by the same signs appearing at the inputs/outputs of the respective circuit sections.

The operation of the circuit will now be described in detail. 8-bit first datawords (r) fed from an error-correcting circuit section are shifted at the symbol clock (fsym) into and along the shift register 4 and are output as a 3-byte or 24-bit parallel signal. The 24-bit parallel signal is latched by the latch circuit 5 at the clock (fMW/2) of three-symbol cycle. That is, three bytes of signals "08", "1A", and "93" are latched by the latch circuit 5 at the rising edge, between times 3 and 4, of the clock (fMW/2) shown in Fig. 28. Of the three bytes of parallel signals, the first byte (8 bits) "08" is input to DH11 - DH4 of the selector 6, and the four bits of "1" on the MSB side of the second byte are input to DH3 - DH0 of the selector 6. Further, the last byte (8 bits) "93" is input to DL11 - DL4 of the selector 6, and the four bits of "A" on the LSB side of the second byte are input to DH3 - DH0, respectively. As a result, between time 4 and the first half of time 5 in Fig. 28, the selector 6 outputs a 12-bit parallel signal "081". Between the second half of time 5 and time 6, the selector 6 outputs a 12-bit parallel signal "93A".

With the above operation, the three 8-bit first datawords "08", "1A", and "93" are word-converted to two 12-bit second datawords, "081" and "93", by dividing the second of the three first datawords into two and appending the respective halves to the LSBs of the first and third bytes of the first dataword. Likewise, the three bytes of the first dataword, "41", "DE", and "F2", latched by the latch circuit 5 at the rising edge, between times 6 and 7, of the clock (fMW/2) in Fig. 28, are word-converted by the selector 6 to two second datawords "41D" and "F2E".

Next, we will describe in detail the operation for converting the 12-bit second datawords to 15-bit codewords. For the convenience of explanation, it is assumed that, at time 4 in Fig. 28, the output Q' of the one-word delay in the MSB controller 8 is low, and that the DSV value for the codeword sequence up to the converted second dataword immediately preceding "081" is 0.

In this condition, when the second dataword "081" is input to the encoder 7 during the period from time 4 to the first half of time 5, the encoder 7 outputs a signal, 8BC9, in accordance with the conversion tables shown in Figs. 11 to 26, the signal having a total of 16 bits, i.e. a codeword formed from the LSB to the 14th bit and the CDS signal and Q signal, one bit each, associated with the codeword. To describe the contents of the signal, of the four bits "1000" corre-

sponding to "8", the MSB represents the Q signal, "0" for a low level and "1" for a high level. Further, of "1000" corresponding to "8", the bit immediately preceding the MSB represents the CDS signal for the codeword, "0" indicating CDS = -1 and a low level and "1" indicating CDS = +1 and a high level. The remaining two bits of the "1000" corresponding to "8", plus the 12 bits "BC9", a total of 14 bits, constitute the data-converted codeword which has 14 bits of "00101111001001" from the 14th bit to the LSB.

Of the signals thus created, the Q signal and the CDS signal are input, along with the DSV control signal (i), to the MSB controller 8 which then determines and outputs the MSB of the codeword in accordance with the operation hereinafter described. The DSV control signal (i) is set at "1" (high level) if the DSV is to be dispersed in the positive direction and at "0" (low level) if the DSV is to be dispersed in the negative direction. In the present embodiment, the DSV control signal is set at a high level for the duration of times 4, 5, and 6 and at a low level for the duration of times 7, 8, and 9, so that the CDS is controlled to give +1 for encoding the second datawords "081" and "93A" and -1 for encoding the second datawords "41D" and "F2E".

The operation of the MSB controller 8 will now be described in detail. First, using the EXOR circuit A, it is checked whether the CDS value of the codeword currently output agrees with the direction in which the DSV is to be dispersed; if they agree, a 0 is output, and if they do not agree, a 1 is output, thereby making the CDS value of the codeword agree with the dispersing direction of the DSV. Note, however, that the above output condition is based on the assumption that encoding is performed with the start point of the codeword at a low level at the time of NRZI modulation. Also note that the MSB needs to be determined by referencing the Q' signal (a 0 for a low level and a 1 for a high level) indicating the end level of the previous NRZI-modulated codeword. The output of the EXOR circuit A and the Q' signal are both input to the EXOR circuit B, and when the Q' signal is "0" (indicating that the NRZI-modulated word level at the end of the previous codeword is low), the output level of the EXOR circuit A appears unchanged at the output of the EXOR circuit B. On the other hand, when the Q' signal is "1" (indicating that the NRZI-modulated word level at the end of the previous codeword is high), since the polarity of the CDS of the codeword is inverted after NRZI modulation, the output level of the EXOR circuit A appears inverted at the output of the EXOR circuit B. The output of the EXOR circuit B is supplied as the MSB of the codeword to the parallel/serial converter 9.

To describe the above operation as applied to the present embodiment, when the second dataword "081" is input to the encoder 7, the CDS signal output from the encoder 7 is "0", and the DSV control signal



(i) is at a high level ("1") that causes the DSV to disperse in the positive direction, as can be seen from Fig. 28, so that the EXOR circuit A outputs a high level signal ("1"). At this time, the Q' signal that indicates the end level of the previous NRZI-modulated codeword is at a low level ("0"), so that the EXOR circuit B outputs a 1 as the MSB of the codeword.

As a result, a 15-bit parallel signal "1001011101" is loaded into the parallel/serial converter 9 in the middle of time 5 when the clock (fMW) goes low. The loaded bits are then output serially at the channel clock (fCH) from the parallel/serial converter 9 to form a code sequence with the MSB at the top of the sequence. The code sequence output from the parallel/serial converter 9 is fed to the NRZI modulator 10 where the polarity of the signal is inverted each time a "1" is input, the resulting signal being shown in Fig. 28(k). Here, with +1 as a high level and -1 as a low level, the CDS can be calculated as +1, which indicates that the DSV of the code sequence is in the positive dispersing direction.

With the above operation, the 12-bit dataword is data-converted to the 15-bit codeword in accordance with the DSV control signal, but it is further necessary to check the end level of the NRZI-modulated codeword, as previously described. This is accomplished by the following operation.

The Q signal from the encoder 7 and the MSB signal from the EXOR circuit B are input to the EXOR circuit C in the MSB controller 8. When the MSB is "0", the Q signal appears unchanged at the output of the EXOR circuit C. On the other hand, when the MSB is "1", the number of inversions that occur in the NRZI modulation increases by one as the number of 1s in the codeword increases by one, and therefore, the Q signal is inverted for output. During the NRZI modulation, the polarity is inverted between positive and negative in accordance with the level of the connected signal. Therefore, the output of the EXOR circuit C is input to the EXOR circuit D along with the Q' signal indicating the word end level of the previous NRZI-modulated codeword, and when the Q' signal is "0" (indicating the word end level after NRZI modulation is low), the output signal of the EXOR circuit C appears unchanged at the output of the EXOR circuit D. On the other hand, when the Q' signal is "1" (indicating the word end level after NRZI modulation is high), the output of the EXOR circuit C is inverted through the EXOR circuit D. The output of the EXOR circuit D is supplied to the one-word delay as a signal indicating the end level of the NRZI-modulated codeword for the immediately following data conversion.

To describe the above operation as applied to the present embodiment, when the second dataword "081" is input to the encoder 7, the Q signal output from the encoder 7 is "1", and the MSB output from the EXOR circuit B is also "1", as can be seen from Fig. 28, so that the output of the EXOR circuit D is at

a low level ("0"). At this time, the signal Q' that indicates the end level of the previous NRZI-modulated codeword is at a low level ("0"), and therefore, the EXOR circuit D outputs a signal "0" indicating that the end level of the NRZI-modulated codeword is low, the signal "0" being input at the clock (fMW) to the one-word delay through which the signal is delayed by one encoding cycle. By repeating the above operation for every m/n data conversion with one word delay at each time, the end level of each codeword can be checked correctly for continuous sequences of codewords.

As described above, the data that has been word-converted by the selector 6 from 8-bit first datawords to 12 bit second datawords is converted by the encoder 7 to a 16-bit codeword, which is further converted by the MSB controller 8 to a 15-bit codeword, capable of determining the dispersing direction of the DSV as desired by the DSV control signal (i), by converting the two bits on the MSB side of the 16-bit codeword to a one-bit signal that determines the polarity of the CDS. Likewise, subsequent second datawords "93A", "41D", and "F2E" are respectively input to the encoder 7 and converted to the signal shown in Fig. 28(j), with their CDSs being controlled in accordance with the DSV control signal (i). As a result, the DSV value at the codeword end obtained at the output of the NRZI modulator 10 has a variation width  $p - p_2$  over four data conversion cycles as shown in Fig. 28(k), the resulting signal thus being made to synchronize with the DSV control signal.

The power spectrum of the digital signal is dependent on the state transition probability, and by keeping the DSV variation cycle at a constant value, the state transition occurring at the DSV variation cycle becomes high, thus making it possible to obtain a spectrum having high power at frequencies corresponding to the DSV variation cycle. In the present embodiment, the cycle of the DSV control signal is selected to be equal to four m/n data conversion cycles. However, if the signal cycle is set equal to about 10 data conversion cycles, it will be possible to obtain a relatively low frequency signal corresponding to the DSV variation cycle synchronized with the digital data, and such a low frequency signal can be used as a tracking pilot signal that will become necessary when the track width is reduced. Fig. 29 is a diagram illustrating the power spectrum obtained when first datawords constructed from 8-bit M-sequence random signals expressed as  $X^{23} + X^5 + 1$  are input in a circuit constructed in accordance with the first embodiment but with the cycle of the DSV control signal set equal to ten m/n data conversion cycles. As can be seen from Fig. 29, the resulting spectrum has no DC content (DC-free) and, at the same time, exhibits high power only at frequencies corresponding to the cycle of the DSV control signal.

We will now describe a digital magnetic record-

ing/reproducing apparatus that can be constructed into a system optimized for the above-described data conversion method.

Digital magnetic recording/reproducing apparatus such as DAT, digital VTR, etc. have the characteristic of being insusceptible to system variation in the sense that degradation in the signal-to-noise ratio does not lead to degradation in the audio and video reproduction performance as long as 1s and 0s can be distinguished. On the other hand, there is some danger with such digital apparatus that only a single bit error in a large volume of information may entirely change the contents of the information. Therefore, in digital magnetic recording/reproducing apparatus, it is essential to employ error-correcting codes for correction of errors caused on the transmission channel. Usually, error-correcting codewords are recorded in error-correcting code blocks separated from one another by a synchronizing signal as shown in Fig. 30. In Fig. 30, the numeral 21 is the synchronizing signal for separating one error-correcting code block from another, 22 is an ID signal for the block identified by the track number or the synchronizing signal, 23 is a parity-check codeword for checking whether the ID signal is correctly reproduced, 24 is an audio/video sector, and 25 is an error-correcting code. Rotary head type digital magnetic recording/reproducing apparatus usually have about 100 such blocks per track, each block being separated by the synchronizing signal.

The following description deals with a method of setting the amount of information for each block.

The synchronizing signal 21 serves not only as a signal for separating each error-correcting code block but also as a signal for executing word synchronization for decoding the codeword, encoded and recorded as previously described, into the original dataword. The synchronizing signal thus has a very important role, and therefore, a unique signal that does not usually appear in the recorded signal sequences is often used as the synchronizing signal. This unique signal can only be obtained by reconvert the data-converted codeword. According to the data conversion method of the first embodiment in which 8-bit first datawords are first converted to 12-bit second datawords and then converted to 15-bit codewords, the synchronizing signal length corresponds to 1.5 bytes in the original first datawords. Therefore, if the synchronizing signal sector is constructed from one-byte synchronizing signal data plus 0.5 byte obtained by dividing the first dataword, these components would become separated at the time of decoding, so that the 0.5 byte in the synchronizing signal sector would cause a fixed error and therefore, the one byte data immediately following the synchronizing signal data would also cause a fixed error. This problem may be solved by inserting a dummy dataword of one byte immediately following the first dataword (which may

be formed from a fixed pattern) used for the synchronizing signal.

However, it is not advantageous in terms of space utilization to add a dummy dataword in a limited package. Therefore, in the recording/reproducing apparatus employing the data conversion method of the first embodiment, the first dataword used for the synchronizing signal is constructed from a fixed pattern of one byte, and the immediately following first dataword is constructed from a fixed pattern of four bits on the MSB side and data (e.g., a cue signal, track address, etc.) of four or less bits on the LSB side. Ordinary 8-bit datawords are mapped starting with the first dataword of the third byte. The pattern of the synchronizing signal sector may be set in any pattern suitable for the reconversion performed after ordinary data conversion.

We will now describe a method of setting the number of first datawords for each block separated by the synchronizing signal. According to the data conversion method of the first embodiment,  $r$ -bit first datawords are first word-converted to  $m$ -bit second datawords and then data-converted to  $n$ -bit codewords. This requires word synchronization for every  $x$  bits, where  $x$  is the least common multiple of  $r$  and  $m$ . For example, in the present embodiment,  $r = 8$  and  $m = 12$ , and hence the least common multiple is 24 bits, so that the first codewords are word-synchronized for every three bytes. Since word synchronization is performed for every three bytes in each block, as described above, if the number of first datawords in the block is not an integral multiple of 3, the first dataword remaining after dividing the number by 3 will cause a fixed error. Therefore, in the recording/reproducing apparatus employing the data conversion method of the first embodiment, the number of first datawords per block is selected to be equal to an integral multiple of  $x/r$ .

The following describes a method of converting the reproduced 15-bit codeword back to the original 12-bit second dataword in the recording/reproducing apparatus employing the above-described data conversion method.

Fig. 32 is a diagram illustrating an example of a circuit configuration for decoding the reproduced 15-bit code-word into the original 12-bit second dataword ( $m$ ), employed in the recording/reproducing apparatus using the data conversion method of the first embodiment. In Fig. 32, the reference numeral 11 is an NRZI demodulator for NRZI-demodulating the reproduction signal transmitted by a reproduction channel clock; 12 is a serial/parallel converter for converting the NRZI-demodulated serial signal, fed from the NRZI demodulator 11, to a 15-bit parallel signal by using a reproduction word clock which is word-synchronized by a synchronizing signal appended to the top of each block; 13 is a first decoder for accepting at its input the 10 bits  $n_1$  on the MSB side of the

15-bit codeword (n) output from the serial/parallel converter 12 and for decoding the n1 into a dataword that forms the eight bits on the MSB side of the second dataword; and 14 is a second decoder for accepting at its input the five bits n2 on the LSB side of the 15-bit codeword (n) output from the serial/parallel converter 12 and for decoding the n2 into a dataword that forms the four bits on the LSB side of the second dataword. The numeral 15 designates a third decoder for decoding the 15-bit codeword, output from the serial/parallel converter 12, into 12-bit decoded data. The third decoder 15 is constructed to perform one-to-one decoding when the five bits on the LSB side is of a prescribed type.

Furthermore, the reference numeral 16 indicates an LSB discriminating circuit which accepts at its input the five bits on the LSB side of the 15-bit codeword output from the serial/parallel converter 12, and which discriminates the type of the codeword and outputs a control signal (o) designating the classification type; and 17 refers to a selector which selects either the 12-bit dataword having decoded data from the first decoder 13 and the second decoder 14 or the 12-bit dataword from the third decoder 15 by using the control signal supplied from the LSB discriminating circuit 17 as a select SW, and which generates the second dataword (m) after decoding.

The operation of this embodiment will now be described. The reproduction signal is NRZI-demodulated by the NRZI demodulator 11 and fed to the serial/parallel converter 12 through which the demodulated serial signal is converted to a 15-bit codeword (n). Of the 15 bits in the codeword (n), the 10 bits n1 on the MSB side are entered to the first demodulator 13, and the five bits n2 on the LSB side are fed to the second demodulator 14 as well as to the LSB discriminating circuit 16. On the other hand, all the 15 bits of the codeword (n) are loaded directly into the third demodulator 15.

We will now describe in detail the operation of decoders for decoding the 15-bit codeword back into the 12-bit second dataword. From Figs. 8 to 10, the five-bit codeword n2 on the LSB side of the 15-bit codeword can be classified in relation to the demodulated 4-bit dataword m2, as shown in Fig. 33. The codewords classified as the first LSB code group in Fig. 33 correspond to the LSB codewords n2 in the first and second coding groups in Figs. 8 and 9 as well as in the first n1 group for m1 = "BB" to "E7" in the third coding group in Fig. 10, and each n1 in the n1 group is related to one decoding data m1 within the limits of the first LSB code group. Further, the codewords classified as the second LSB code group correspond to the LSB codewords classified as the second n1 group in the third coding group as well as the fourth n1 group for m1 = "EE" to "FF" in the same coding group. The codewords classified as the third LSB code group correspond to the LSB codewords clas-

sified as the third n1 group while the codewords classified as the fourth LSB code group correspond to the LSB codewords in the n1 group for m1 = "E8" to "FF". Note, however, that the n1 group in the second to the fourth LSB code groups overlaps with the n1 group in the first LSB code group, and that, in some cases, a plurality of m2 are mapped to one LSB codeword n2.

Now, the first decoder 13 decodes the 10 bits on the MSB side into an eight-bit dataword. In this case, the top bit in the 10-bit codeword is a control bit for controlling the DSV during demodulation and may therefore be disregarded at the time of decoding. Thus, the remaining nine bits are decoded. The decoding is performed on the codewords in the first and second coding groups in Figs. 8 and 9 as well as in the first n1 group for m1 = "BB" to "E7" in the third coding group in Fig. 10, and the decoded 8-bit dataword is applied to V11 - V4 on the selector 17 as data representing the eight bits on the MSB side of the second dataword. On the other hand, the second decoder 14 decodes the five bits on the LSB side into a four-bit dataword. In this case, the decoding is performed on the codeword n2 in the first LSB code group. The decoded 4-bit dataword is applied to V3 - V0 on the selector 17 as data representing the four bits on the LSB side of the second dataword. The third decoder 15 decodes the input 15-bit codeword into a 12-bit dataword. In this case also, the top bit on the MSB side is excluded, as in the case of the first decoder 13, and the remaining 14 bits are decoded. The decoding is performed only when the five-bit LSB codeword falls in one of the second to the fourth LSB code groups, and the decoded 12-bit dataword is applied to W11 - W0 on the selector 17.

The selector 17 is switched to select either the dataword supplied from the first decoder 13 and second decoder 14 or the dataword supplied from the third decoder 15, and outputs the selected dataword as the decoded second dataword; the switching of the selector 17 is controlled by a control signal supplied from the LSB discriminating circuit 16. Of the 15 bits in the codeword, the five bits n2 on the LSB side are input to the LSB discriminating circuit 16 to discriminate the type of the LSB codeword. For example, if the LSB codeword n2 is a codeword discriminated as belonging to one of the second to the fourth LSB code groups, the LSB discriminating circuit 16 outputs a control signal (o) indicating the discriminated type and applies it to the select SW on the selector 17. When no control signal (o) is received, the selector 17 selects the eight-bit MSB dataword V11 - V4 supplied from the first decoder 13 and the four-bit LSB dataword V3 - V0 supplied from the second decoder 14 and outputs the resulting 12-bit dataword V. On the other hand, when the control signal (o) is received, the selector 17 is switched to select the 12-bit dataword W decoded by the third decoder 15.

Thus, the selector 17 outputs the 12-bit dataword  $m$  obtained by reconvertng the codeword that was encoded in accordance with the tables shown in Figs. 8 to 10. As described, the 15-bit codeword is decoded on a one-to-one basis to the 12-bit dataword only when the five-bit LSB codeword  $n_2$  is discriminated as belonging to one of the second to the fourth LSB code groups in Fig. 33; otherwise, the 15-bit codeword is decoded by dividing it into 10 bits on the MSB side and five bits on the LSB side. This construction serves to reduce the possibility of the error propagation that may occur between decoded first datawords due to a single bit detection error in the 15-bit codeword.

In the above decoding method, the discrimination of the five-bit LSB codeword is determined by which of the two major groups, the first LSB code group or the second to fourth LSB code group, the LSB codeword belongs to. However, in an alternative method, the types of codeword may be classified into three major groups, for example, the first LSB code group, the second LSB code group, and the third/fourth LSB code group, and four decoders, i.e. the first to the fourth decoders, may be provided, the outputs of these decoders being selected accordingly by using a control signal from the LSB discriminating circuit. Such configuration may somewhat increase the circuit complexity compared to that of the above embodiment, but will serve to further reduce the possibility of error propagation between decoded first datawords.

Thus, according to the first embodiment, eight-bit first datawords are first word-converted to 12-bit second datawords, and then, the 12-bit second datawords are converted to 15-bit codewords, each having bits of  $CDS = +1$  or  $-1$ , by executing word-synchronization for every two second datawords, which is the least common multiple of the first and second datawords. In the conversion process, the first byte of the first dataword is mapped to the eight bits on the MSB side of the first of the two second datawords, and the four bits on the MSB side of the second byte of the first dataword are mapped to the four bits on the LSB side of the first of the two second datawords. For the second of the two second dataword, the third byte of the first dataword is mapped to the eight bits on the MSB side, while the four bits on the LSB side of the second byte of the first dataword are mapped to the four bits on the LSB side of the second of the two second datawords, thus accomplishing the 8/12 word-conversion. Thereafter, each 12-bit dataword is converted to a 15-bit codeword (12/15 data conversion). This encoding method permits the 8/10 encoding of non-divided first datawords and the 4/5 encoding of divided first datawords, which serves to reduce the possibility of the error propagation that may occur, due to a single bit error in the codeword, between first datawords during reverse conversion at

the time of decoding. Furthermore, when this data conversion method is applied to a recording/reproducing apparatus, it is possible to construct a system capable of efficient recording of data without requiring redundant bits, the system being constructed such that the number of first datawords for each block separated by a synchronizing signal is set at a multiple of  $r/x$  and that the first dataword as the synchronizing signal at the beginning of the block is formed from a fixed pattern and the immediately following first dataword is formed from a fixed pattern of four bits on the MSB side and data of four or less bits (e.g., cue signal, track address, etc.) on the LSB side.

On the other hand, when decoding the 15-bit codeword into the 12-bit second dataword, the 15-bit codeword is divided into 10 bits on the MSB side and five bits on the LSB side, and the 10 bits are decoded into eight bits by the first decoder and the five bits are decoded into four bits by the second decoder, while the 15 bits are decoded into 12 bits by the third decoder. The discriminating circuit discriminates the type of the five bits on the LSB side of the 15-bit codeword and outputs a control signal indicating the discriminated type, on the basis of which the decoded data from the decoders are selected to reconstruct the second dataword. Thus, the codeword is decoded by reverse conversion of 10/8 and 5/4, except when the LSB codeword falls under specific conditions. This has the effect of reducing the possibility of the error propagation that may occur between decoded first datawords due to a detection error in the codeword.

#### (Embodiment 2)

A second embodiment of the invention will now be described below. Suppose a code of dataword length = 12 and codeword length = 14 with one bit added to form a code with  $T_{max}/T_{min} = 5$ . Here, let  $d = 0$  and  $k = 4$ . The NRZI(F) rule is used to construct the code. To satisfy  $k = 4$  in each codeword, the number of successive 0s in the codeword is limited to 4, and since one bit is inserted between codewords, the number of successive 0s is limited to 2 on the MSB side and 1 on the LSB side.

To form a DC-free code, 4096 pairs of codewords, each pair having codewords of different CDS polarities, should be provided. There are 2481 codewords with  $CDS = 0$ , 2169 codewords with  $CDS = +2$ , and 1888 codewords with  $CDS = -2$ , which satisfy the above conditions. Hence, 2481 codewords with  $CDS = 0$  and 1615 pairs of codewords with  $CDS = \pm 2$ , which differ only in MSB, are used to suppress the dispersion of DSV to achieve DC-free modulation. Code conversion tables thus constructed are shown in Figs. 34 to 49. The data given in Figs. 34 to 49 represent binary digital signals in hexadecimal notation. For every 12-bit input data (dataword), there are out-

put a total of 16 bits, i.e. a 14-bit codeword, one-bit data (hereinafter represented by Q) indicating the number of inversions performed on the NRZI-modulated codeword, and the CDS (a zero or nonzero bit) of the codeword.

Fig. 50 is a diagram illustrating an example of a circuit configuration implementing the second embodiment. In Fig. 50, the reference numeral 33 is an encoder for converting 12-bit digital data (dataword) to 16-bit digital data shown in Figs. 34 to 49; 34 and 36 are NOT gates; 35, 37, 38, and 39 are EXOR gates; 40, 42, and 48 are flip-flops; 41 and 46 are selectors; 43 is a parallel/serial converter for converting 14-bit or 15-bit parallel data to a serial data sequence; 44 is a counter; 45 is a four-input NAND gate; and 47 is an NRZI modulator for processing the codeword, converted to serial data, so that the signal polarity is inverted each time a 1 is input.

Fig. 51 shows a code select table used to determine a code to be selected in accordance with the current and the previous DSV control signal values, the CDS value of the codeword just selected, and the previous Q' signal.

Fig. 52 is a diagram illustrating code conversion and DSV value variation according to the second embodiment. In Fig. 52, (a) is a pilot signal (write at "1"), (b) is a DSV control signal (positive direction at "1"), (c) is input data (12 bits), (d) is a code select signal Q', (e) is a selected codeword, (f) is a signal waveform to be recorded, and (g) is a DSV value at the end of each codeword.

Fig. 53 is a diagram illustrating the recording format of a magnetic recording/reproducing apparatus according to the second embodiment. In Fig. 53, subcode signals, etc. are recorded in the subdata areas (SUB1, SUB2), and video and audio signals are recorded in the main data area (MAIN). Pilot signals are recorded in the subdata areas (SUB1, SUB2).

The circuit operation of the second embodiment will now be described below with reference to Fig. 50.

First, when the pilot area signal output from the flip-flop 48 is "0", i.e. when data other than that for the subdata area is to be encoded for recording, 12-bit data is input to the encoder 33 where the 12-bit data is converted to a 14-bit codeword (parallel) by using the code select signal Q' supplied from the flip-flop 42. The resulting 14-bit codeword is supplied to the parallel/serial converter 43. The encoder 33 also outputs a Q signal which is supplied to the selector 41. On the other hand, the selector 46 selects "10" by the input pilot area signal "0", which sets the load value of the counter 14 to "0010", and the counter 14 outputs a load CLK of one CLK width to the parallel/serial converter 43 for every 14 CH-CLKs. The parallel/serial converter 43 converts the input 14-bit parallel codeword to serial data which is fed to the NRZI modulator 47. At this time, the output of the NOT gate 34, i.e. the LSB, is input to the parallel/serial converter

43, but since a load CLK is input for every 14 CH-CLKs, the LSB is not output from the parallel/serial converter 43. The serial codeword input to the NRZI modulator 47 is NRZI-modulated for output. The Q signal output from the encoder 33 is input to the selector 41 which selects the Q signal by the pilot area signal and supplies it to the flip-flop 42.

On the other hand, when the pilot area signal output from the flip-flop 48 is "1", i.e. when data for the subdata area is to be encoded for recording, 12-bit data is input to the encoder 33 which converts the 12-bit data to a 14-bit codeword (parallel) by using the code select signal Q' supplied from the flip-flop 42. The resulting 14-bit codeword is supplied to the parallel/serial converter 43. The encoder 33 also supplies a Q signal to the selector 41 and the NOT gate 34, and a CDS signal to the EXOR gate 35. The selector 46 selects "01" by the input pilot area signal "1", which sets the load value of the counter 14 to "0001", and the counter 14 supplies a load CLK of one CLK width to the parallel/serial converter 43 for every 15 CH-CLKs. The parallel/serial converter 43 converts the input 14-bit parallel codeword and LSB to serial data which is supplied to the NRZI modulator 47. The serial codeword input to the NRZI modulator 47 is NRZI-modulated for output. The DSV control signal is input to the flip-flop 40 and the EXOR gate 39, the output of the flip-flop 40 being coupled to the other input of the EXOR gate 39. The output of the EXOR gate 39, i.e. the exclusive OR sum of the current DSV control signal and the previous DSV control signal, is supplied to one input of the EXOR gate 38. The EXOR gate 35 EXORs the CDS signal output from the encoder 33 with the Q' signal. The output of the EXOR gate 35 is inverted through the NOT gate 36 and applied to one input of the EXOR gate 37. The EXOR gate 37 EXORs the output of the NOT gate 36 with the Q' signal and supplies the result to the other input of the EXOR gate 38. The EXOR gate 38 EXORs the outputs of the EXOR gates 37 and 39 and supplies the result to the other input of the selector 41. In this manner, the signal selected in accordance with the codeword select table is output as a Q' signal. The Q' signal is selected by the pilot area signal and is fed to the flip-flop 42 to form a code select signal Q' for the next coding.

Suppose, for example, that the pilot area signal is "0", the input data is "3FF", the previous polarity is "1", and the Q' signal is "0". In this case, the 14-bit codeword output from the encoder 33 is "11001000010111"; the CDS is -2 and the DSV is also -2. As a result, the Q signal "0" is output. Next, when data "200" is input, since the Q' signal, i.e. the previous Q signal, is "0", the selected codeword is "01110011011010"; the CDS is +2 and the DSV is 0. Next, when the pilot area signal and the DSV control signal both go to a "1" and data "E11" is input, since the Q' signal is "1", the output codeword is

"11010111010101", and a Q signal of "0" and a CDS signal of "0" are output; CDS and DSV are both 0. When data "715" is input, since the Q' signal is "1", the selected codeword is "00101111101001" and the LSB is "0"; CDS and DSV are both +1. Next, when data "BFC" is input, since the Q' signal is "1", the selected codeword is "10100111101110" and the LSB is "1"; CDS is +1 and DSV is +2. The above operation is repeated for the subdata areas where the pilot area signal is "1", thereby achieving a modulation method in which the DSV varies at the cycle of the DSV control signal.

Using the above-described modulation method, a tracking servo pilot signal is recorded at two places within one track. Therefore, in the subdata areas (SUB1, SUB2), one LSB bit is added to the 14-bit codeword to form a 15-bit codeword, as described previously, and modulation is performed on the codeword including the pilot signal, while for other areas, the 14-bit codeword is directly modulated.

According to the format shown in Fig. 53, two subdata areas are provided within one track, and the pilot signals are recorded in these areas. Alternatively, three or more pilot signal recording areas may be provided within one track in order to enhance the tracking accuracy with a narrower track. It will also be appreciated that the pilot signal may be recorded in any portion within the subdata areas.

As described, according to the second embodiment, 12-bit datawords are each converted to a 14-bit codeword which, after NRZI modulation, has a succession of the same level, more than one bit long and five bits at maximum, and provides CDS = 0, +2 or -2, the CDS value being controlled to suppress the dispersion of DSV, thus accomplishing a DC-free modulation method. Furthermore, in areas where pilot signals are recorded, one bit is added to the 14-bit codeword to form codewords of CDS =  $\pm 1$ , and the modulation is performed so that the DSV varies in synchronism with the DSV control signal to produce a tracking control pilot signal. This eliminates the need for ATF areas, the areas where only tracking control signals are recorded. Moreover, since one bit is added to the codeword and the pilot signal is recorded in a restricted area, it is not necessary to substantially raise the recording rate, and therefore, high density recording is achieved. Furthermore, since the modulation method is basically the same for both the pilot signal areas and other areas, the configuration does not involve any appreciable increase in the circuit complexity.

As this invention may be embodied in several forms without departing from the spirit of essential characteristics thereof, the present embodiment is therefore illustrative and not restrictive, since the scope of the invention is defined by the appended claims rather than by the description preceding them, and all changes that fall within metes and bounds of

the claims, or equivalence of such metes and bounds thereof are therefore intended to be embraced by the claims.

## Claims

1. A data conversion method for word-converting an r-bit first dataword to an m-bit second dataword ( $r < m$ ) and converting the word-converted m-bit second dataword to an n-bit codeword ( $m < n$ ), comprising the steps of:
  - dividing a sequence of first datawords into groups of x bits where x is the least common multiple of r and m;
  - dividing into x/m an arbitrary first dataword selected from a group which consists of x/r first datawords; and
  - forming the m-bit second dataword by appending  $r/(x/m)$ -bit data, obtained by dividing the first dataword into x/m, to the LSB or MSB side of one or other of the non-divided first datawords.
2. A recording/reproducing apparatus employing the data conversion method of claim 1, comprising:
  - means for formatting blocks so that each block separated by a synchronizing signal contains the first datawords of the number equal to an integral multiple of x/r.
3. A recording/reproducing apparatus employing the data conversion method of claim 1, comprising:
  - means for mapping r bits and  $r/(x/m)$  bits to a second dataword corresponding to a synchronizing signal; and
  - means for mapping the remaining bits of the divided first dataword to a signal, other than the synchronizing signal, that can complete as data with the remaining bits alone.
4. A recording/reproducing apparatus employing the data conversion method of claim 1, comprising:
  - first decoding means for decoding n1 bits in the reproduced n-bit codeword into r bits, the reproduced n-bit codeword being divided into n1 bits and n2 bits for reverse conversion into the m-bit second dataword;
  - second decoding means for decoding the n2 bits into  $r/(x/m)$  bits;
  - third decoding means for decoding the n bits into the m bits;
  - identifying means for identifying the type of bits at prescribed positions in the n-bit codeword and for outputting an identification signal designating the identified type; and

means for selecting decoded data from the first, second, or third decoding means on the basis of the identification signal supplied from the identifying means and for outputting the decoded second dataword.

5. A data converting apparatus for word-converting an  $r$ -bit first dataword to an  $m$ -bit second dataword ( $r < m$ ) and converting the word-converted  $m$ -bit second dataword to an  $n$ -bit codeword ( $m < n$ ), comprising:

means for dividing a sequence of first datawords into groups of  $x$  bits where  $x$  is the least common multiple of  $r$  and  $m$ ;

means for dividing into  $x/m$  an arbitrary first dataword selected from a group which consists of  $x/r$  first datawords; and

means for forming the  $m$ -bit dataword by appending  $r/(x/m)$ -bit data, obtained by dividing the first dataword into  $x/m$ , to the LSB or MSB side of one or other of the non-divided first datawords.

6. A data conversion method for converting an  $m$ -bit dataword to a codeword consisting of a greater number of bits than  $m$ , comprising the steps of:

a first data conversion process wherein the  $m$ -bit dataword is converted to an  $n$ -bit codeword having a finite digital sum variation (DSV); and

a second data conversion process wherein the DSV is controlled at a desired value by appending one bit to the  $n$ -bit codeword obtained by the first data conversion process.

7. A data conversion method for dividing digital data first into 12-bit datawords and then converting the 12-bit datawords to 14-bit codewords,

wherein the number of successive 0s between a 1 and a 1 in each of the codewords is limited to 4 within the codeword and to 3 between codewords, and codewords having a codeword digital sum (CDS) of 0 are directly related to the 12-bit data, while for codewords having a CDS of +2 or -2, two codewords that differ only in MSB are paired and related to the 12-bit data, the two codewords being switched selectively by using polarity signals of the respective codewords.

8. A data conversion method for dividing digital data first into 12-bit datawords and then converting the 12-bit datawords to 15-bit codewords,

wherein one bit is appended to a 14-bit codeword converted in accordance with the data conversion method of claim 7, to form a pair of codewords, one having a CDS of +1 and the other having a CDS of -1, with the number of successive 0s between a 1 and a 1 in the code sequence

being limited to 4, and one or other of the pair of codewords is selected in accordance with a DSV control signal with the DSV being made to vary in synchronism with the DSV control signal.

9. A magnetic recording/reproducing apparatus for recording multiple kinds of data in partitioned areas, wherein

when converting digital data to codewords for recording, the data conversion method of claim 8 is used for recording one or more areas within one track and the data conversion method of claim 7 is used for recording the other areas in the same track.

10. A data converting apparatus for converting an  $m$ -bit dataword to a codeword consisting of a greater number of bits than  $m$ , comprising:

means for converting the  $m$ -bit data to an  $n$ -bit codeword having a finite digital sum variation (DSV) as a first data conversion process; and

means for controlling the DSV at a desired value by appending one bit to the  $n$ -bit codeword obtained by the first data conversion process as a second data conversion process.

11. A data conversion method for word-converting first datawords, the method comprising, for each group of  $n$  first datawords, dividing  $x$  of the datawords by  $(n-x)$  and adding each divided part to a respective one of the other datawords in the group to form respective second datawords, and converting the second datawords to codewords of greater length.

Fig. 1  
Prior Art

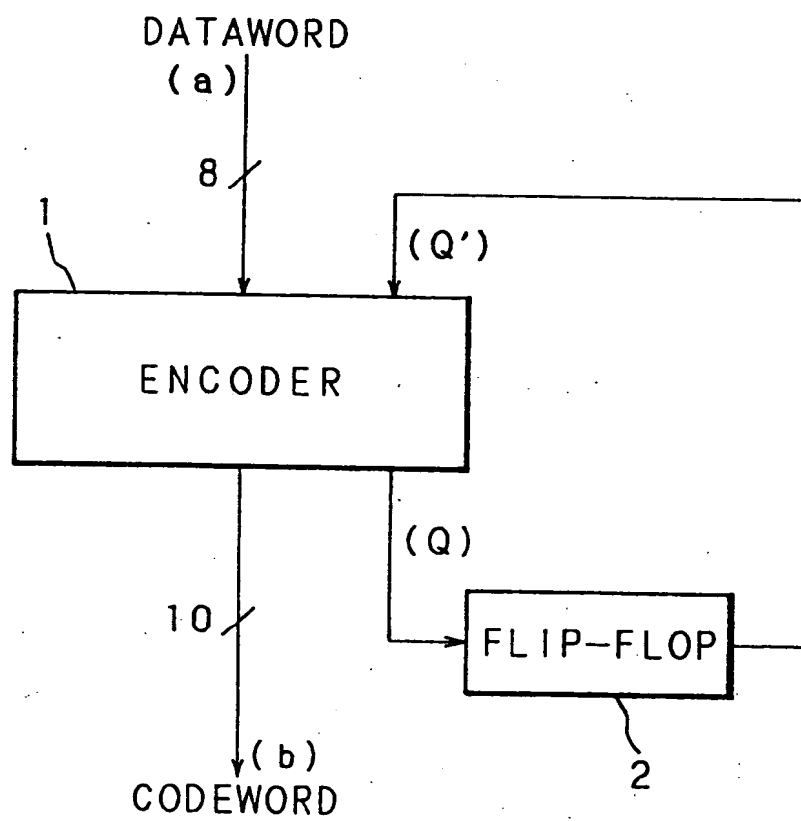




Fig. 2  
Prior Art

		Q' = -1		Q' = 1			
	DATAWORD (MSB-LSB)	CODEWORD (MSB-LSB)	DC	Q	CODEWORD (MSB-LSB)	DC	Q
00	00000000	0101010101	0	1	0101010101	0	-1
01	00000001	0101010111	0	-1	0101010111	0	1
02	00000010	0101011101	0	-1	0101011101	0	1
03	00000011	0101011111	0	1	0101011111	0	-1
04	00000100	0101001001	0	-1	0101001001	0	1
05	00000101	0101001011	0	1	0101001011	0	-1
06	00000110	0101001110	0	1	0101001110	0	-1
07	00000111	0101011010	0	1	0101011010	0	-1
08	00001000	0101110101	0	-1	0101110101	0	1
09	00001001	0101110111	0	1	0101110111	0	-1
0A	00001010	0101111101	0	1	0101111101	0	-1
0B	00001011	0101111111	0	-1	0101111111	0	1
0C	00001100	0101101001	0	1	0101101001	0	-1
0D	00001101	0101101011	0	-1	0101101011	0	1
0E	00001110	0101101110	0	-1	0101101110	0	1
0F	00001111	0101110101	0	-1	0101110101	0	1
.	.	.	.	.	.	.	.
.	.	.	.	.	.	.	.
.	.	.	.	.	.	.	.
.	.	.	.	.	.	.	.
.	.	.	.	.	.	.	.
.	.	.	.	.	.	.	.
F0	11110000	1101010101	0	-1	1101010101	0	1
F1	11110001	1101010111	0	1	1101010111	0	-1
F2	11110010	1101011101	0	1	1101011101	0	-1
F3	11110011	1101011111	0	-1	1101011111	0	1
F4	11110100	1101001001	0	1	1101001001	0	-1
F5	11110101	1101001011	0	-1	1101001011	0	1
F6	11110110	1101001110	0	-1	1101001110	0	1
F7	11110111	1101011010	0	-1	1101011010	0	1
F8	11111000	1111100101	2	-1	0111100101	-2	-1
F9	11111001	1111100111	2	1	0111100111	-2	1
FA	11111010	1111101101	2	1	0111101101	-2	1
FB	11111011	1111101111	2	-1	0111101111	-2	-1
FC	11111100	1111111001	2	1	0111111001	-2	1
FD	11111101	1111111011	2	-1	0111111011	-2	-1
FE	11111110	1111111110	2	-1	0111111110	-2	-1
FF	11111111	1111101010	2	-1	0111101010	-2	-1



Fig. 4  
Prior Art

MARGIN2	
SUB2	POST AMBLE2
	SUB DATA2
	PRE AMBLE3
ATF2	IBG4
	ATF2
	IBG3
MAIN	MAIN DATA
	PRE AMBLE2
ATF1	IBG2
	ATF1
	IBG1
SUB1	POST AMBLE1
	SUB DATA1
	PRE AMBLE1
MARGIN1	

Fig. 5  
Prior Art

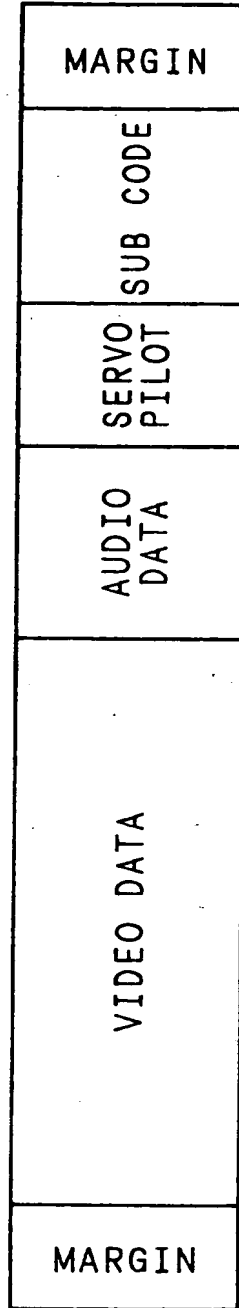


Fig. 6

CDS	-1	+1	-3	+3	-5	+5
NUMBER OF CODEWORDS	2481	2169	1888	1231	909	410

Fig. 7

CONDITION OF $n_1$	A CDS=0			B CDS=+2			C CDS=-2		D CDS=+4		E CDS=-4	
	A1	A2	A3	B1	B2	B3	C1	C2	D1	D2	E1	
NUMBER OF $n_1$ CODEWORDS	102	14	4	51	17	3	116	4	18	2	68	
$n_2$ CODEWORD MATCHING WITH $n_1$	02			02	02							
	05	05		05	05	05						
	07	07		07	07	07						
	09	09	09				09					
	0A	0A	0A	0A	0A	0A						
	0B	0B	0B				0B					
	0D	0D	0D	0D	0D	0D						
	0E	0E	0E				0E					
	0F	0F	0F	0F	0F	0F						
	12	12	12				12	12				
	15	15	15				15	15				
	17	17	17				17	17				
	19	19	19	19	19	19						
	1A	1A	1A				1A	1A				
	1B	1B	1B	1B	1B	1B						
	1D	1D	1D				1D	1D				
	1E	1E	1E	1E	1E	1E						
	1F	1F	1F				1F	1F				
				01					01			
				03	03				03	03		
				06	06	06			06	06		
							11	11			11	
							13	13			13	
							16	16			16	

NOTE : SUBDIVISIONS OF A TO E GROUPS ARE EXECUTED FOR EACH  $n_1$  GROUP IN ACCORDANCE WITH "0" RUN-LENGTH AT LSB SIDE OF  $n_1$ .

Fig. 8

	m1	n1	n2 CODEWORD															
	DATAWORD	GROUP	m2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E
1	00 ~ 73	A GROUP (A1, A2)	07090A0B0D0E0F121517191A1B1D1E1F															

F i g . 9

	m1 DATAWORD	n1 GROUP	n2 CODEWORD																
			m2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
2	74 ~ BA	1st n1 GROUP C1	09 0B 0E 121517 1A 1D1E																
		2nd n1 GROUP B1	07 0A 0D 0F 19 1B 1F																



Fig. 10

3	m1 DATAWORD	n1 GROUP	n2 CODEWORD															
			m2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E
	BB ~ E7	1st n1 GROUP C1	09 08 0E 121517 1A 1D1E															
		2nd n1 GROUP B1	01 03 05 06															
		3rd n1 GROUP E1	11 13 16															
	E8 ~ ED	1st n1 GROUP C1	11 13 16 111316 11 1316															
		2nd n1 GROUP B1	01 03 05 06															
		3rd n1 GROUP E1	11 13 16															
	EE ~ FF	1st n1 GROUP C1	11 13 16 111316 11 1316															
		2nd n1 GROUP D1	01 03 06															
		3rd n1 GROUP E1, C2	11 13 16															
		4th n1 GROUP B2, B3	05															

Fig. 11

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00	847	849	84A	84B	84C	84D	84E	84F	855	857	885	889	85A	85D	85E	85F
01	88A	889	88E	88B	88D	88A	88F	88F	88B	88B	88B	889	88B	88B	88B	88B
02	88E	889	88E	88B	88D	88A	88F	88F	88B	88B	88B	889	88B	88B	88B	88B
03	894	89A	89E	89B	89D	89A	89F	89F	89B	89B	89B	899	89A	89D	89E	89F
04	89A	89E	89E	89B	89D	89A	89F	89F	89B	89B	89B	899	89A	89D	89E	89F
05	89E	89E	89E	89B	89D	89A	89F	89F	89B	89B	89B	899	89A	89D	89E	89F
06	8B2	8B6	8B2	8B6	8B2	8B6	8B2	8B6	8B3	8B3	8B3	8B9	8B3	8B3	8B3	8B3
07	8B6	8B6	8B6	8B6	8B6	8B6	8B6	8B6	8B3	8B3	8B3	8B9	8B3	8B3	8B3	8B3
08	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B3	4B3	4B3	4B9	4B3	4B3	4B3	4B3
09	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B3	4B3	4B3	4B9	4B3	4B3	4B3	4B3
0A	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B3	4B3	4B3	4B9	4B3	4B3	4B3	4B3
0B	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B6	4B3	4B3	4B3	4B9	4B3	4B3	4B3	4B3
0C	512	512	512	512	512	512	512	512	512	512	512	512	512	512	512	512
0D	512	512	512	512	512	512	512	512	512	512	512	512	512	512	512	512
0E	91C	91C	91C	91C	91C	91C	91C	91C	91C	91C	91C	91C	91C	91C	91C	91C
0F	524	524	524	524	524	524	524	524	524	524	524	524	524	524	524	524

Fig. 12

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
10	92A7	52A9	12AA	D2AB	92AD	D2AE	12AF	52B8	D2B8	52B8	792B	D2BA	12BB	52BD	12BE	D2BF
11	52E7	2E99	2EAI	12EB	52ED	12EE	D2EF	2F21	D2F2	52F2	752F	D2FA	12FB	52FD	D2FE	12FF
12	93A7	53A9	34AA	D3AB	93AD	D3AE	13AF	53B8	D3B8	53B8	793B	D3BA	13BB	53BD	D3BE	13BF
13	53E7	3E99	3EAI	D3EB	53ED	D3EE	13EF	3F21	D3F2	53F2	793F	D3FA	13FB	53FD	D3FE	13FF
14	95A7	55A9	52AA	D5AB	95AD	D5AE	15AF	55B8	D5B8	55B8	795B	D5BA	15BB	55BD	D5BE	15BF
15	55E7	2E99	52EA	D5EB	55ED	D5EE	15EF	55F2	D5F2	55F2	795F	D5FA	15FB	55FD	D5FE	15FF
16	55C7	55C9	56AA	D5CB	55CD	D5CE	15CF	55D8	D5D8	55D8	795D	D5DA	15DB	55DD	D5DE	15DF
17	56A7	56A9	56CA	D5CB	56CD	D5CE	15CF	56D8	D5D8	56D8	796D	D5DA	16DB	56DD	D5DE	16DF
18	96A7	56A9	64AA	D6AB	96AD	D6AE	16AF	56B8	D6B8	56B8	796B	D6BA	16BB	56BD	D6BE	16BF
19	56E7	6E99	6EAA	D6EB	56ED	D6EE	16EF	56F2	D6F2	56F2	796F	D6FA	16FB	56FD	D6FE	16FF
1A	96E7	6E99	6EAA	D6EB	56ED	D6EE	16EF	56F2	D6F2	56F2	796F	D6FA	16FB	56FD	D6FE	16FF
1B	57A7	7A99	74AA	D7AB	57AD	D7AE	17AF	57B8	D7B8	57B8	797B	D7BA	17BB	57BD	D7BE	17BF
1C	97A7	7A99	74AA	D7AB	57AD	D7AE	17AF	57B8	D7B8	57B8	797B	D7BA	17BB	57BD	D7BE	17BF
1D	57E7	7E99	7EAI	D7EB	57ED	D7EE	17EF	57F2	D7F2	57F2	797F	D7FA	17FB	57FD	D7FE	17FF
1E	9A7	5A9	2AA	D2AB	92AD	D2AE	12AF	52B8	D2B8	52B8	792B	D2BA	12BB	52BD	D2BE	12BF
1F	5A67	79A9	69DA	A6BA	5A6D	A6BE	D6AF	9A72	D6A7	9A72	75A7	D6A7	A7BA	5A7D	D6A7	1A7F

Fig. 13

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
20	5AC79AC9D	ACA1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5	ACD1ACB5
21	9C475C491	C4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A	DC4A1DC4A
22	5CA79CA9D	CAA1CAB5	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1	CAB5CAA1
23	9CE75CE91	CEA1DCEB	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1	DCEBCEA1
24	5D479D49D	D4A1D4AB	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1	D4ABD4A1
25	9DA75DA91	DAA1DDEB	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1	DDEBDAA1
26	5DE79DE9D	DEA1DF2A	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1	DF2ADEA1
27	9F275F291	F2A1F6A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1	F6A1F2A1
28	5FC79FC9D	FCA1FCB5	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1	FCB5FCA1
29	62277A22	22A1E26A	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1	E26A22A1
2A	267762C92	76A1C92A	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1	C92A76A1
2B	64477A44	77A1A44A	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1	A44A77A1
2C	4A7764A9	776A1A9E	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1
2D	64E77A4E	77A1E4E9	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1
2E	4A7764A9	776A1A9E	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1	A9E776A1
2F	64E77A4E	77A1E4E9	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1	E4E977A1

Fig. 14

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
30	A547	6549	254A	E54A	BA54	E54D	254F	654F	E552	6557	A559	E55A	255B	655D	255E	E55F
31	65A7	A5A9	E5A2	A25A	BA5A	E5AD	25AF	A5B2	E5B2	A5B7	65B9	E5BA	25BB	A58D	E5BE	E5BF
32	A5E7	65E9	E25E	E5E2	BA5E	E5ED	25EF	65F2	E5F2	A5F7	65F9	E5FA	25FB	A5FD	E5FE	E5FF
33	672A	729A	E72A	A27A	BA72	E72D	272F	672F	E722	A737	6739	E73A	273B	A73D	E73E	E73F
34	A767	6769	E27E	E76A	BA76	E76D	276F	676F	E762	A777	6779	E77A	277B	A77D	E77E	E77F
35	A7C7	67C9	E27C	E7CA	BA7C	E7CD	27CF	67CF	E7C2	A7D7	67D9	E7DA	27DB	A7DD	E7DE	E7DF
36	A927	6929	E29A	E92A	BA92	E92D	292F	692F	E922	A937	6939	E93A	293B	A93D	E93E	E93F
37	69C7	A9C9	E96A	E29C	BA96	E96D	296F	696F	E962	A977	6979	E97A	297B	A97D	E97E	E97F
38	69C7	A9C9	E96A	E29C	BA96	E96D	296F	696F	E962	A977	6979	E97A	297B	A97D	E97E	E97F
39	A47A	6A49	E4AA	E2A4	BA4A	E4AD	2A4F	6A4F	E4A2	A4D7	6A49	E4DA	2A4B	A4DD	E4DE	E4DF
3A	6AA7	A6A9	E2AA	E6AA	BA6A	E6AD	2AAF	6AA7	E6A2	A6B7	6AB9	E6BA	2ABF	A6BD	E6BE	E6BF
3B	AAE7	6AE9	E2AE	E6AE	BA6A	E6AD	2AAF	6AA7	E6A2	A6B7	6AB9	E6BA	2ABF	A6BD	E6BE	E6BF
3C	6BB7	A6B9	E2BA	E6BA	BA6B	E6BD	2ABF	6BB7	E6B2	A6B7	6AB9	E6BA	2ABF	A6BD	E6BE	E6BF
3D	ABBA	76BBA	992B	BA9A	ABBA	E6BD	2ABF	6BB7	E6B2	A6B7	6AB9	E6BA	2ABF	A6BD	E6BE	E6BF
3E	6BE7	A6BE	E2BE	E6BE	BA6B	E6BD	2ABF	6BB7	E6B2	A6B7	6AB9	E6BA	2ABF	A6BD	E6BE	E6BF
3F	6D27	AD29	EED2	A2D2	BA6D	E6DD	2ADF	6D2F	E6D2	A76D	6D39	E76A	2D3B	AD3D	EED3	EEDF

Fig. 15

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
40	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
41	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
42	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
43	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
44	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
45	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
46	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
47	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
48	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
49	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
4A	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
4B	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
4C	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
4D	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
4E	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A
4F	AD67	6DC7	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A	6E7A

Fig. 16

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
50	75E7	B5E9	F5EA	35EB	75ED	35ED	F5EF	B5F2	35F5	B5F7	75F9	35FA	F5FB	B5FD	F5FE	35FF
51	B727	7729	372A	F72B	B72D	F72D	B72F	7732	F735	B737	8739	F73A	A73B	773D	B73E	F73F
52	7767	B769	F76A	376B	776D	376D	F76F	B772	3775	B777	7779	377A	F77B	B77D	F77E	377F
53	77C7	B7C9	F7CA	37CB	77CD	37CD	F7CF	B7D2	37D5	B7D7	77D9	37DA	F7DB	B7DD	F7DE	37DF
54	7927	B929	F92A	392B	792D	392D	F92F	B932	3935	B937	7939	393A	F93B	B93D	F93E	393F
55	B967	7969	F96A	396B	B96D	F96D	B96F	7972	F975	B977	7979	397A	F97B	B97D	F97E	397F
56	B9C7	79C9	F9CA	39CB	B9CD	F9CD	B9CF	79D2	F9D5	B9D7	79D9	39DA	F9DB	B9DD	F9DE	39DF
57	A477	BA49	FA4A	3A4B	7A4D	3A4D	FA4F	BA52	3A55	B7A5	7A59	3A5A	FA5B	B7A5	FA5E	3A5F
58	BA77	7A79	FA7A	3A7B	BA7D	3A7D	FA7F	7AB2	3AB5	B7AB	7AB9	3ABA	FA7B	B7AB	FA7E	3ABF
59	7AE7	BAE9	FAEA	3AEB	7AED	3AED	FAEF	BAF2	3AF5	B7AF	7AF9	3AFA	FAFB	B7AF	FAFE	3AFF
5A	BB47	7B49	FB4A	3ABB	BB4D	3ABD	FB4F	7B52	3BB5	B7B5	7BB9	3BBB	FB7B	B7B5	FB7E	3BBF
5B	7BA7	BBA9	FBAA	3BAB	7BAD	3BAD	FB7B	BBB2	3BB5	B7BB	7BB9	3BBB	FB7B	B7BB	FB7E	3BBF
5C	BBE7	7BE9	FBEB	3ABE	BBED	3ABE	FB7B	7BF2	3BBF	B7BF	7BB9	3BBB	FB7B	B7BB	FB7E	3BBF
5D	BD27	7D29	FBD2	3ABD	BD2D	3ABD	FBD2	7D32	3BD5	B7BD	7BD9	3BDD	FB7B	B7BD	FB7E	3BDF
5E	7D67	BD69	FBD6	3ABD	BD6D	3ABD	FBD6	7D72	3BD5	B7BD	7BD9	3BDD	FB7B	B7BD	FB7E	3BDF
5F	7DC7	BD9C	FDC9	3DCB	7DCD	3DCD	FDCD	BD22	3DD5	B7DD	7DD9	3DDA	FB7B	B7DD	FB7E	3DDF

Fig. 17

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
60	BE47	7E49	3E4A	FE4B	BE4D	FE4E	3E4F	FE49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
61	7E4A	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
62	7E4E	7E4F	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
63	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
64	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
65	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
66	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
67	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
68	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
69	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
6A	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
6B	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
6C	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
6D	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
6E	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49
6F	7E4F	7E49	FE4A	FE4B	7E4D	FE4E	FE4F	7E49	FE43	7E45	BE47	FE49	FE43	7E45	BE47	FE49



Fig. 18

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
70	7387	7389	F38A	338B	738D	338E	F38F	B392	3395	B397	7399	339A	F39B	B39D	F39E	339F
71	7388	7389	F68A	368B	768D	368E	F68F	B692	3695	B697	7699	369A	F69B	B69D	F69E	369F
72	7388	7389	F38A	388B	788D	388E	F38F	B782	F895	7897	B899	F89A	389B	789D	F89E	389F
73	7388	7389	F38A	388B	788D	388E	F38F	B782	F895	7897	B899	F89A	389B	789D	F89E	389F
74	0927	7389	892A	486B	092D	486E	892F	C877	0875	C877	0939	487A	893B	087D	893E	487F
75	0AE7	7389	8AE8	888B	0AED	888E	8AEF	C877	0875	0897	0AF9	889A	8AFB	088D	8AFB	889F
76	0BA7	7389	8BAA	48CB	0BAD	48CE	8BAF	C8D2	48D5	C8D7	0BB9	48DA	8BBB	088D	8BBE	48DF
77	0D67	7389	8D6A	896B	0D6D	896E	8D6F	C972	4975	0977	0D79	897A	8D7B	097D	8D7E	497F
78	0EA7	7389	8EAA	498B	0EAD	498E	8EAF	C992	4995	C997	0EB9	499A	8EDB	099D	8EBE	499F
79	0FE7	7389	8FEA	89CB	0FED	89CE	8FEF	C9D2	89D5	09D7	0FF9	89DA	8FFB	09D9D	8FFF	49D9F
7A	0E70	7389	8E8A	48AB	0E8D	8E8E	8E8F	C9D2	89D5	09D7	0FF9	89DA	8FFB	09D9D	8FFF	49D9F
7B	1A70	7389	1AA4	4B0B	1AD4	4B0E	1AFF	C9D2	89D5	09D7	0FF9	89DA	8FFB	09D9D	8FFF	49D9F
7C	1227	7389	22A8	8B4B	122D	8B4E	22AF	C9D2	89D5	09D7	0FF9	89DA	8FFB	09D9D	8FFF	49D9F
7D	1327	7389	32A8	8BEB	132D	8BEE	32AF	C9D2	89D5	09D7	0FF9	89DA	8FFB	09D9D	8FFF	49D9F
7E	14E7	7389	4EA8	8D2B	14ED	8D2E	4EAF	C9D2	89D5	09D7	0FF9	89DA	8FFB	09D9D	8FFF	49D9F
7F	15A7	7389	55AA	8F4B	15AD	8F4E	55AF	C9D2	89D5	09D7	0FF9	89DA	8FFB	09D9D	8FFF	49D9F

Fig. 19

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
80	D627	0EE9	562A	8EEB	D62D	82DD	562F	0EF2	8EF5	0EF7	D639	8EFA	563B	0EFD	563E	82FF
81	1727	0FA9	72A8	FAB1	72DB	8FAD	EFB2	0FB2	EFB5	0FB7	7398	FBFA	73B0	EFBD	73E3	8F8F
82	1967	0A99	6A50	AB10	6AD9	50A9	66FF	0B21	8FB5	0B71	9795	8BBA	97BD	0BDD	97E7	8F08
83	1AA7	1099	9AA9	10B1	9ADD	0A91	96FF	1121	5151	1171	AB99	1A99	ABBB	11DD	9ABE	11FF
84	1BE7	1D19	9BCA	14B1	BED5	0915	9AEE	1151	5151	1571	BF99	15A9	BFBB	15DD	9BC3	15FF
85	1DC7	1E99	2A51	1EB1	DC2D	051E	2CFF	1F21	5151	1F71	3995	1FA9	3CBB	1FDD	2C3E	1FFF
86	1EE7	1269	2EA9	26B1	2D2D	052E	2EFF	2721	5151	2771	4995	27A9	4CBB	27DD	2E3E	27FF
87	1FA7	12C9	2EA9	26B1	2D2D	052E	2EFF	2721	5151	2771	4995	27A9	4CBB	27DD	2E3E	27FF
88	1677	1369	16A9	36B1	16AD	093C	16FF	3721	5151	3771	5995	37A9	5FBB	37DD	3E3E	37FF
89	1A77	1389	1AA9	36B1	1AD2	093C	1AEE	3921	5151	3971	9995	39A9	5FBB	39DD	3E3E	39FF
8A	2E77	13C9	2EA9	36B1	2EAD	093C	2EFF	3D21	5151	3D71	9995	3DA9	6FBB	3DD3	43E3	3DFF
8B	2A77	1439	2AA9	36B1	2ED4	093C	2AEE	3B21	5151	3B71	9995	3BA9	6FBB	3BDD	43E3	3BFF
8C	2477	14A9	2AA9	36B1	24AD	093C	24EE	3421	5151	3471	9995	34A9	6FBB	34DD	43E3	34FF
8D	2E77	1509	2AA9	36B1	2ED4	093C	2EEE	3521	5151	3571	9995	35A9	6FBB	35DD	43E3	35FF
8E	2677	1549	2AA9	36B1	26AD	093C	26EE	3621	5151	3671	9995	36A9	6FBB	36DD	43E3	36FF
8F	2A77	15E9	2AA9	36B1	27AD	093C	2AEE	3721	5151	3771	9995	37A9	6FBB	37DD	43E3	37FF

F18.20

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
90	28E7D	6669A	8EA5A	566BB	28ED9	566ED	8EFAA	D6722	5675D	D6777	28F99	567A5	A8FBD	D67DA	8FEA8	567FF
91	29A71	689A9	AA2A9	668BB	29AD9	668AD	9AFAA	D6922	96955	16977	29B99	669A5	A9BBB	169DA	9BEE8	969FF
92	EA27D	6C9A6	2A9A6	6CB2B	EA2D9	6CBAD	6A2A6	D6D77	56D5D	D6D77	EA399	56DA5	6A3BB	D6DD6	6A3EE	56DFF
93	2BCE7	769A6	AB2A9	768BB	2BED9	768AD	AB2A6	D7722	7775D	D7777	2B399	777A5	AB3BB	177DA	AB3EE	777FF
94	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
95	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
96	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
97	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
98	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
99	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
9A	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
9B	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
9C	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
9D	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
9E	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF
9F	EEA71	7C9A6	6CEA9	778BB	EEAD9	778AD	6CEA6	D7922	7955D	D7977	ECF99	795A5	6CFBB	D79DD	6CFEE	795FF

F 1 8 . 2 1

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
A0	F9A71E	A979AA	9EAB8B	F9AD9E	9EAE9E	79AF1E	EB225B	95F12E	9EB51E	7FB77F	F9B97F	9EB97F	79B8B1	EBD1E	79B8B1	EBD1E
A1	3A27DF	F09BA2	A59F0B	3A2D5A	5F0E5A	2A2F79	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A2	FB271F	497B2A	9F4B8B	FB2D5A	0E5F0E	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A3	3CE71F	E99BC2	A96FEB	3CEDED	9F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A4	3DA7E1	299BDA	A612B8	3DA66A	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A5	FE27E2	497B2A	9F4B8B	FE2D5A	0E5F0E	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A6	3F27E2	E99BC2	A96FEB	3F2D5A	0E5F0E	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A7	0A47E3	A91A4A	63AB0A	0A4D6A	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A8	0DC724	69988F	0DC88F	0DCDA4	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
A9	0F47E4	89988F	0F4A4A	0F4D6A	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
AA	1447E4	C9999A	144CB1	144CD6	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
AB	19C7E5	69999A	19C88B	19CDA4	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
AC	1B47E5	59999A	1B488B	1B4DA4	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
AD	1E47E5	59999A	1E488B	1E4DA4	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
AE	1C7E6A	69999A	1C788B	1C7DA4	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E
AF	E34727	099634	E3488B	E34DA4	6F4B8B	2F79A2	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E	27DF1E

Fig. 22

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
B0	E647	E749	664A	674B	E64D	674E	E664	E752	6755	E757	E659	675A	665B	E75D	665E	675F
B1	2847	E7E9	A84A	67EB	284D	67E6	E7A8	E7F2	267F	E7F7	E285	67FA	A85B	E7FD	A85E	267F
B2	EC47	28A9	6C4A	A8AB	EC4D	6C4E	E7C4	E7F2	A8B5	E21B	EC59	A8BA	6C5B	28BD	6C5E	EC5F
B3	31C7	E909	B1CA	690B	31CD	690E	E7C1	E912	6915	E917	31D9	691A	B1DB	E91D	691E	31D9
B4	3347	2949	B34A	A94B	334D	A94E	E7C3	E295	A955	E297	3359	A95A	B35B	E29F	A95E	335F
B5	3647	29E9	B64A	A9EB	364D	A9EE	E7C6	E29F	A9F5	E297	3659	A9FA	B65B	E29F	A9FE	365F
B6	F847	E669	784A	6A6B	F84D	6A6E	E784	E7A7	6A75	E7A7	F859	6A7A	B785	E7A7	6A7E	F85F
B7	3C47	2AC9	B8C4	AA8B	3C4D	AA8E	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
B8	0C87	EAC9	8C8A	6ACB	0C8D	6ACE	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
B9	1887	E2B9	88A9	6B8B	188D	6B8E	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
BA	3087	E8B9	B08A	6B8B	308D	6B8E	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
BB	4921	EBC9	C92A	6BCB	4925	6BCB	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
BC	4AE1	E2CA	9CAE	36CB	4AE5	36CA	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
BD	4BA1	E2D0	9CBA	36AD	4BA5	36AD	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
BE	4D61	E4D9	CD6A	36D4	4BD8	36D4	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F
BF	4EA1	E4DE	9CEA	36DE	4BE8	36DE	E7C8	E7A9	AA95	E2A9	3C59	AA9A	B8C9	E7A9	AA9E	3C5F

Fig. 23

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
C0	4FE1	2EE8	CFE3	AE68	8FE5	AE6E	CFE6	2EE2	AE72	2E75	AE70	AE7A	8D73	2E7D	8D76	AE7F
C1	90E1	EE89	10E3	E68B	55E5	E8E1	10E6	EE92	E922	E955	970D	E9A8	8D93	EE9D	8D96	E9FF
C2	91A1	EEC9	11A3	E6CB	55E5	E8E1	11A6	EE92	E922	E955	970D	E9A8	8D93	EE9D	8D96	E9FF
C3	5221	EE69	D223	AF6B	9225	AF6E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
C4	9321	EE89	D133	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
C5	54E1	EEC9	D4E3	AF6B	9225	AF6E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
C6	55A1	EE89	D5A3	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
C7	9621	EE89	D6A3	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
C8	5721	EE89	D7A3	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
C9	5961	EE89	D9A3	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
CA	5AA1	EE89	DA93	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
CB	5BE1	EE89	DB93	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
CC	9C21	EE89	1C23	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
CD	5D21	EE89	DD23	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
CE	5EE1	EE89	DEE3	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F
CF	5FA1	EE89	DFA3	AF8B	9225	AF8E	2266	EEF7	F722	F755	F70F	F7A8	F733	EEF7	F766	AF7F

Fig. 24

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
D0	A161	36A9	2163	B6AB	6165	B6AE	2166	36B2	B6B2	36B7	19F1	B6BA	99F3	336B	99F6	B6BF
D1	A2A1	F709	22A3	770B	62A5	770E	22A6	F712	7715	F717	1A91	771A	99A3	F71D	99A6	B771
D2	A3E1	3749	23E3	B74B	63E5	B74E	23E6	3752	B755	3757	1B71	B75A	99B7	3375	99B6	B775
D3	A421	37E9	24E3	B7EB	64A5	B7EE	24E6	37F2	B7F5	37F7	1BD1	B7FA	99BD	337F	99BD	B77F
D4	A521	F8A9	25E3	B78B	65E5	B78E	25E6	F8B2	78B5	F8B7	1D11	78BA	99D1	F8BD	99D6	B78B
D5	A6E1	3909	26E3	B90B	66E5	B90E	26E6	3912	B915	3917	1E71	B91A	99E7	F91D	99E6	B791
D6	A7A1	F949	27A3	B94B	67A5	B94E	27A6	39F2	79F5	39F7	1ED1	79FA	99F7	F9FD	99F6	B795
D7	A8E1	F9E9	28E3	B9EB	68E5	B9EE	28E6	F9F2	79F5	39F7	1F91	79FA	99F7	F9FD	99F6	B795
D8	A9A1	3A69	29A3	B9AB	69A5	B9AE	29A6	F9F2	79F5	39F7	1E15	BA7A	6153	F9FD	99F6	B795
D9	AA21	FA89	2AA3	B9AB	6A25	BA8E	2AA6	F9F2	79F5	39F7	1E15	BA7A	6153	F9FD	99F6	B795
DA	ABCE	13AC	2BCE	7BAC	6ACE	7BAC	2BCE	7BAC	6ACE	7BAC	2BCE	7BAC	6ACE	7BAC	6ACE	7BAC
DB	ADAE	13B8	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	6ACE	7BAC
DC	ADAE	13B8	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	6ACE	7BAC
DD	ADAE	13B8	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	6ACE	7BAC
DE	ADAE	13B8	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	6ACE	7BAC
DF	ADAE	13B8	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	2DCE	7BAC	6ACE	7BAC	6ACE	7BAC

Fig. 25

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
E0	72A1	3D49	F2A3	BD4B	B2A5	BD4E	F2A6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E1	73E1	3DE9	F3E3	BD4B	B3E5	BD4E	F3E6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E2	7421	3E69	F423	BD4B	B425	BD4E	F426	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E3	7521	3E89	F523	BD4B	B525	BD4E	F526	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E4	76E1	3EC9	F6E3	BD4B	B6E5	BD4E	F6E6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E5	77A1	3F69	F7A3	BD4B	B7A5	BD4E	F7A6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E6	78E1	3F89	F8E3	BD4B	B8E5	BD4E	F8E6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E7	79A1	3FC9	F9A3	BD4B	B9A5	BD4E	F9A6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E8	7A21	390B	F0A3	BD4B	B0A5	BD4E	F0A6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
E9	7B21	3951	F1B3	BD4B	B155	BD4E	F1B6	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
EA	7CE1	3915	F1CE	BD4B	B155	BD4E	F1CE	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
EB	7DA1	3915	F1CE	BD4B	B155	BD4E	F1CE	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
EC	7DA1	3915	F1CE	BD4B	B155	BD4E	F1CE	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
ED	7DA1	3915	F1CE	BD4B	B155	BD4E	F1CE	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
EE	7DA1	3915	F1CE	BD4B	B155	BD4E	F1CE	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F
EF	7DA1	3915	F1CE	BD4B	B155	BD4E	F1CE	3D52	BD55	3D57	E791	BD5A	6793	3D5D	6796	BD5F



Fig. 26

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
F0	1DA1	3391	8DA3	3393	8F45	3396	8DA8	6659	E591	E593	33D1	B591	B3D3	3593	83D6	3596
F1	1F21	33D1	8F28	3393	4513	6883	F26A	5D1A	255D	259D	6635	755D	516B	5D33	3516	5596
F2	1421	3381	4233	3393	58C5	6894	26A6	6B1A	26B1	26B3	6637	76B1	616B	6B33	5166	56B6
F3	18E1	3351	8E33	3393	5B45	6985	26A6	671A	711E	713E	6636	711B	616B	6B33	6637	6371
F4	19A1	3351	9A33	3393	5E45	6995	26A6	751A	2751	2753	6637	751B	751B	753F	7966	7566
F5	1BE1	3351	9B33	3393	61C5	6995	26A6	7F1A	27F1	27F3	6638	77F1	7873	7F33	7966	7756
F6	1E21	3361	8E23	3393	6345	6995	26A6	8B1A	28B1	28B3	6638	8B1B	78D3	8B33	78D6	8666
F7	1EA1	3361	1A33	3393	6455	6995	26A6	911A	2913	2913	6639	911B	7993	9133	7996	9166
F8	1321	3361	3233	3393	6455	6995	26A6	951A	2953	2953	663A	951B	7993	9533	7996	9566
F9	1621	3361	6233	3393	6455	6995	26A6	9F1A	29F3	29F3	663A	9F1B	7993	9F33	7996	9F66
FA	16C2	3361	6C33	3393	6455	6995	26A6	971A	2A73	2A73	663C	971B	7993	9733	7996	9766
FB	190E1	3361	90E33	3393	6455	6995	26A6	991A	2A93	2A93	663C	991B	7993	9933	7996	9966
FC	1A1A1	3361	1A33	3393	6455	6995	26A6	AD1A	2AD1	2AD3	663C	AD1B	7993	AD33	7996	AD66
FD	13321	3361	33233	3393	6455	6995	26A6	AD1A	2AD1	2AD3	663C	AD1B	7993	AD33	7996	AD66
FE	13621	3361	6233	3393	6455	6995	26A6	AD1A	2AD1	2AD3	663C	AD1B	7993	AD33	7996	AD66
FF	13C21	3361	8C33	3393	6455	6995	26A6	BD1A	2BD1	2BD3	663C	BD1B	7993	BD33	7996	BD66

Fig. 27

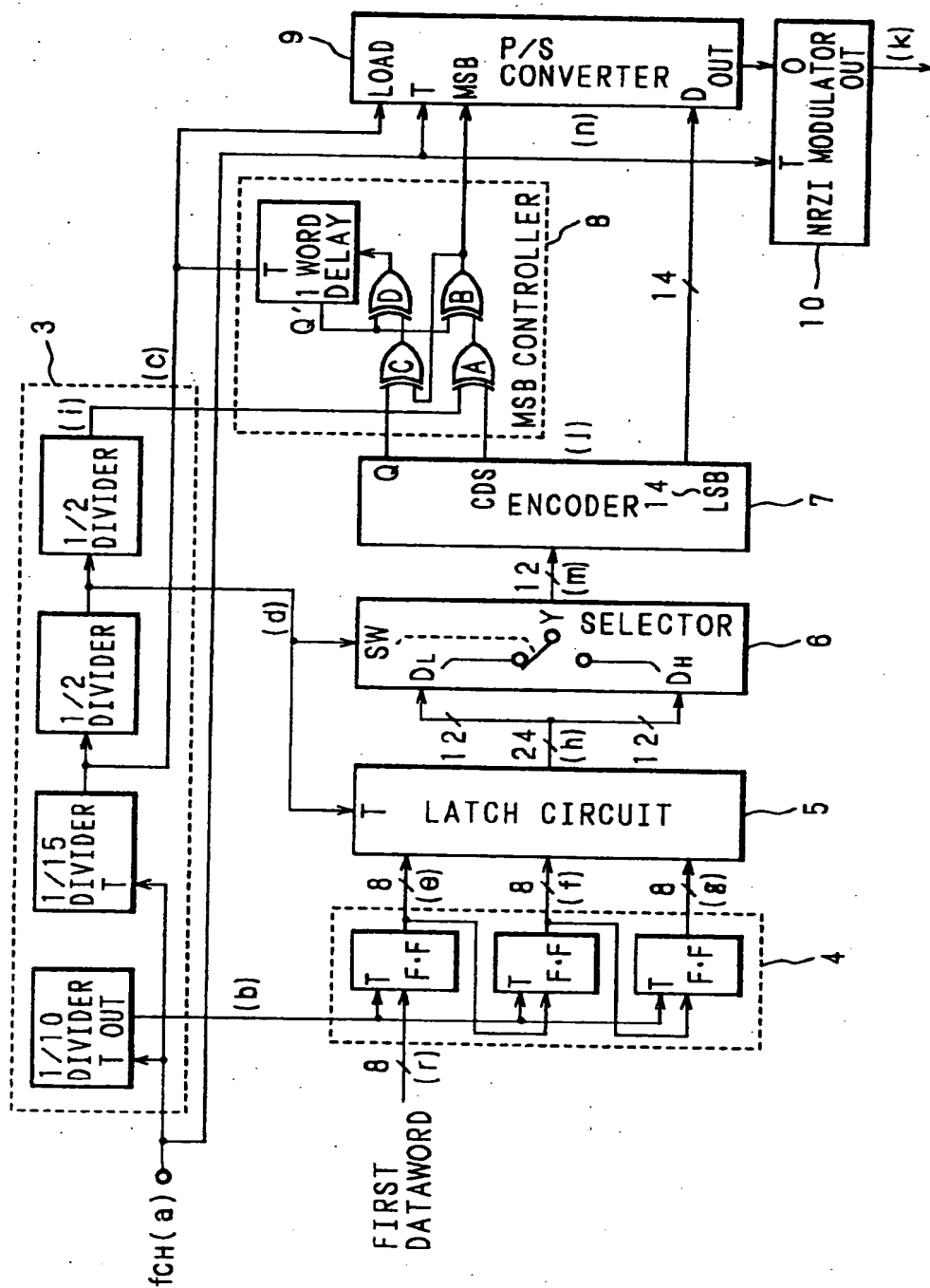


Fig. 28(A)

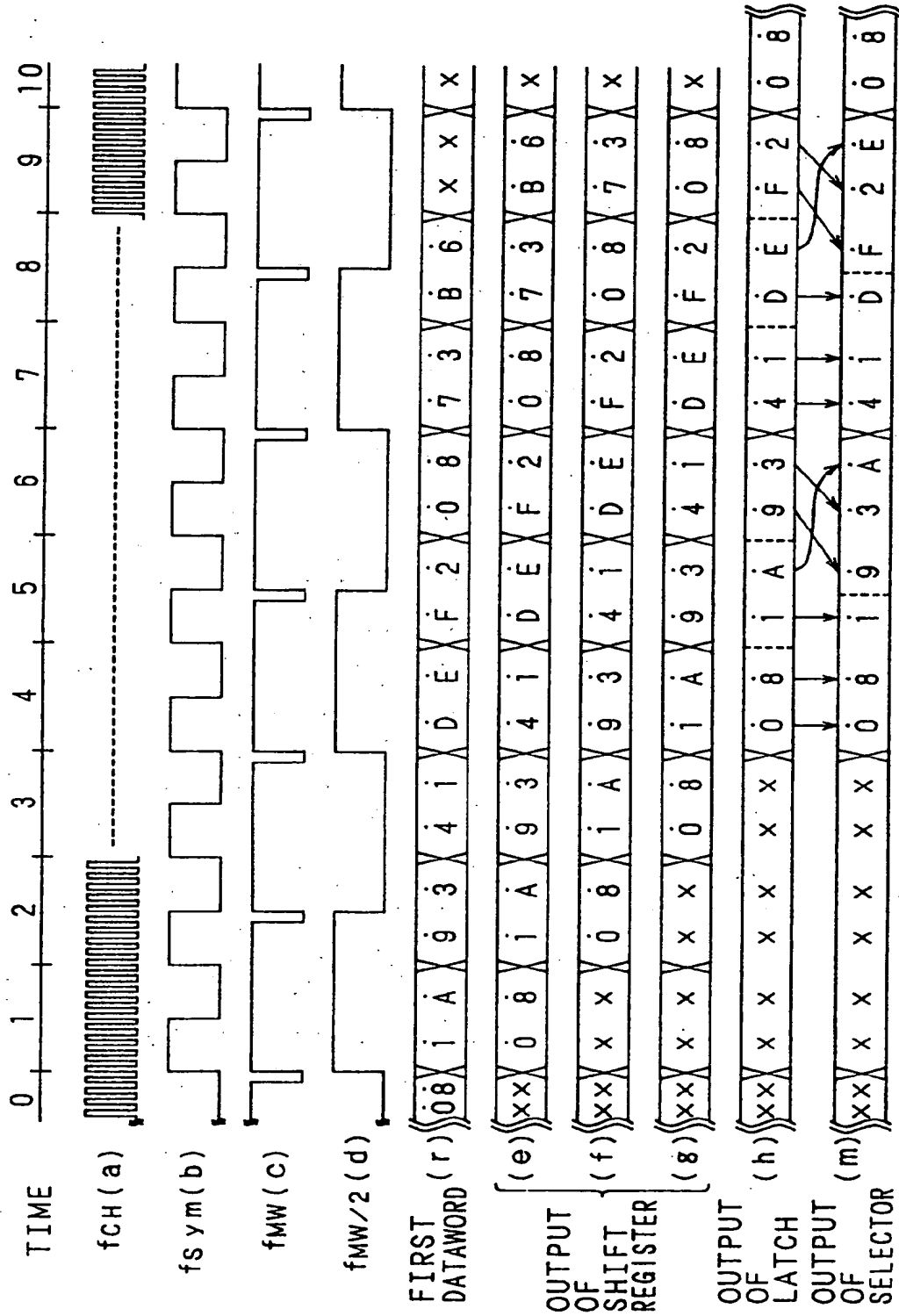


Fig. 28(B)

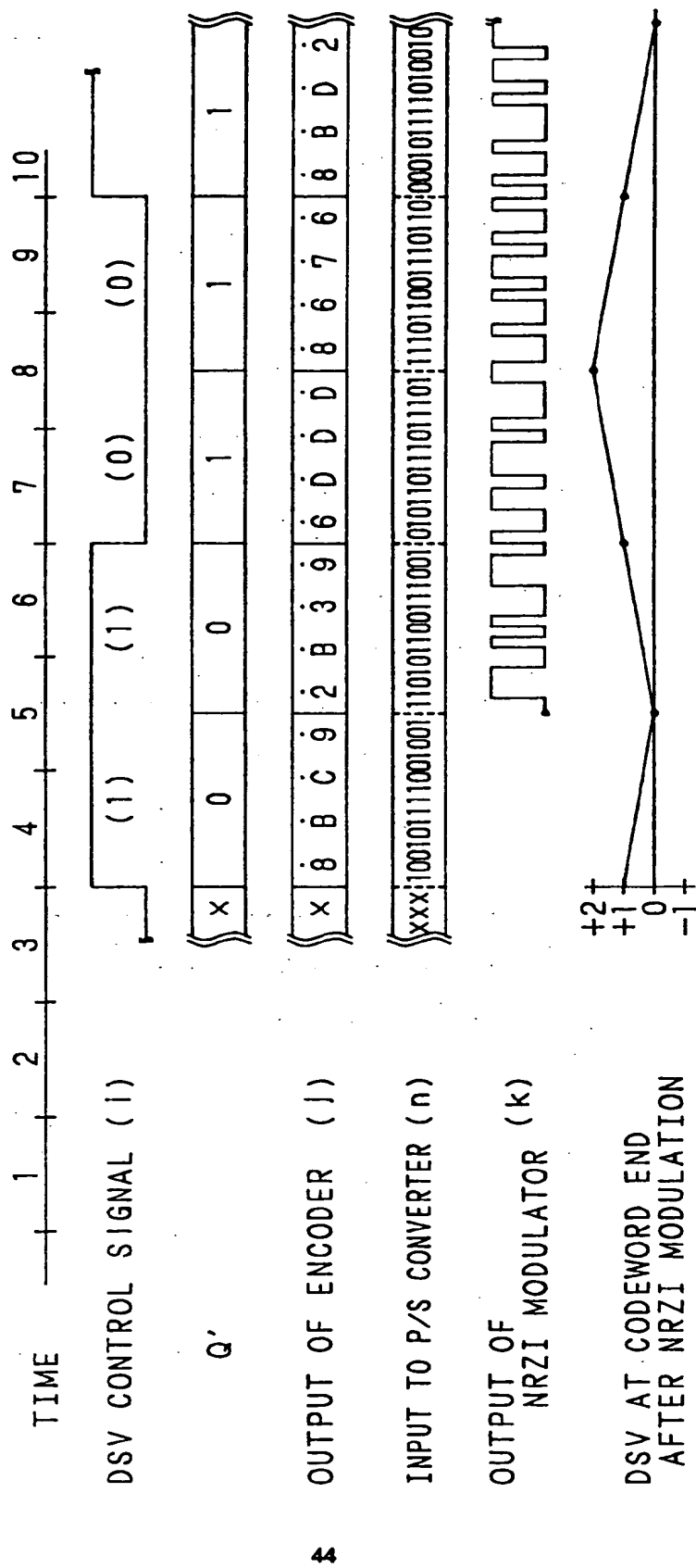


Fig. 29

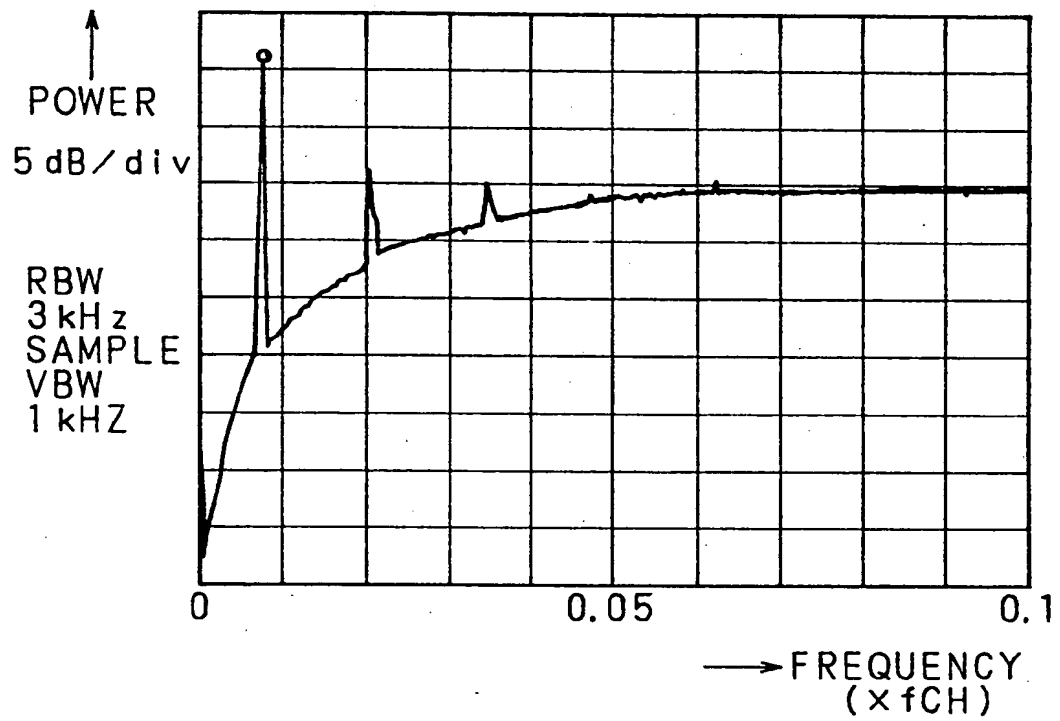
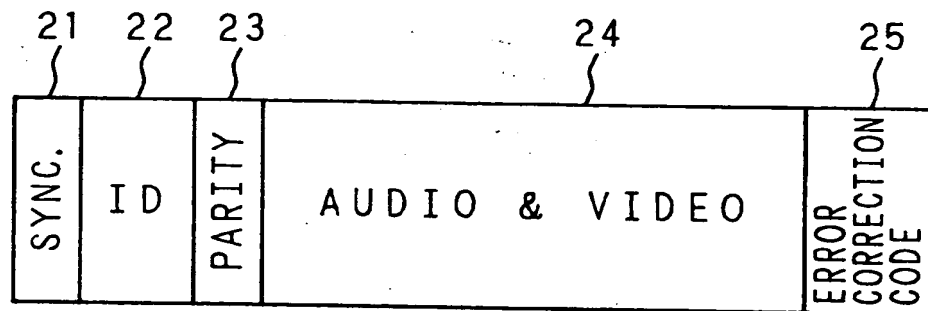


Fig. 30



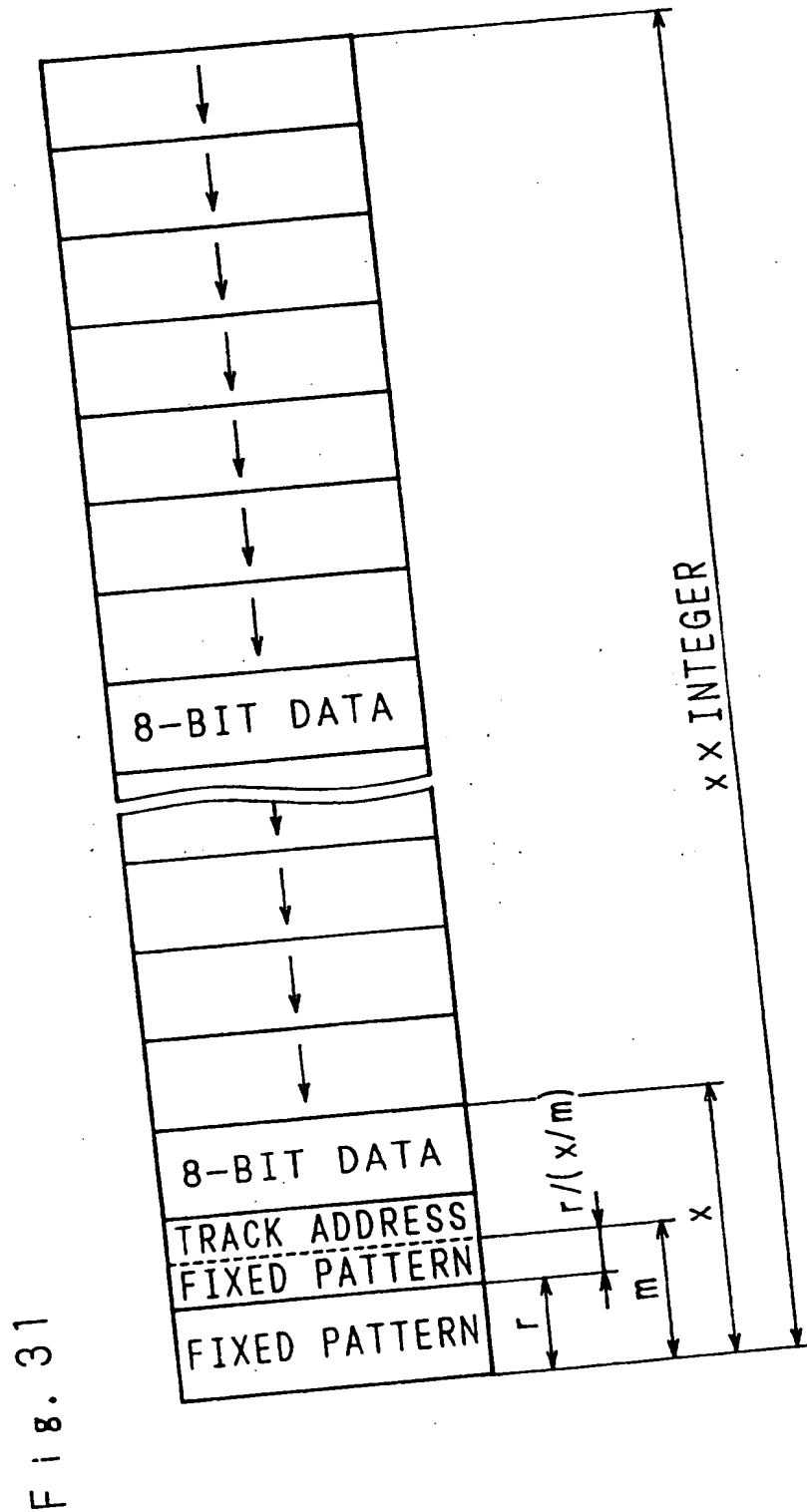
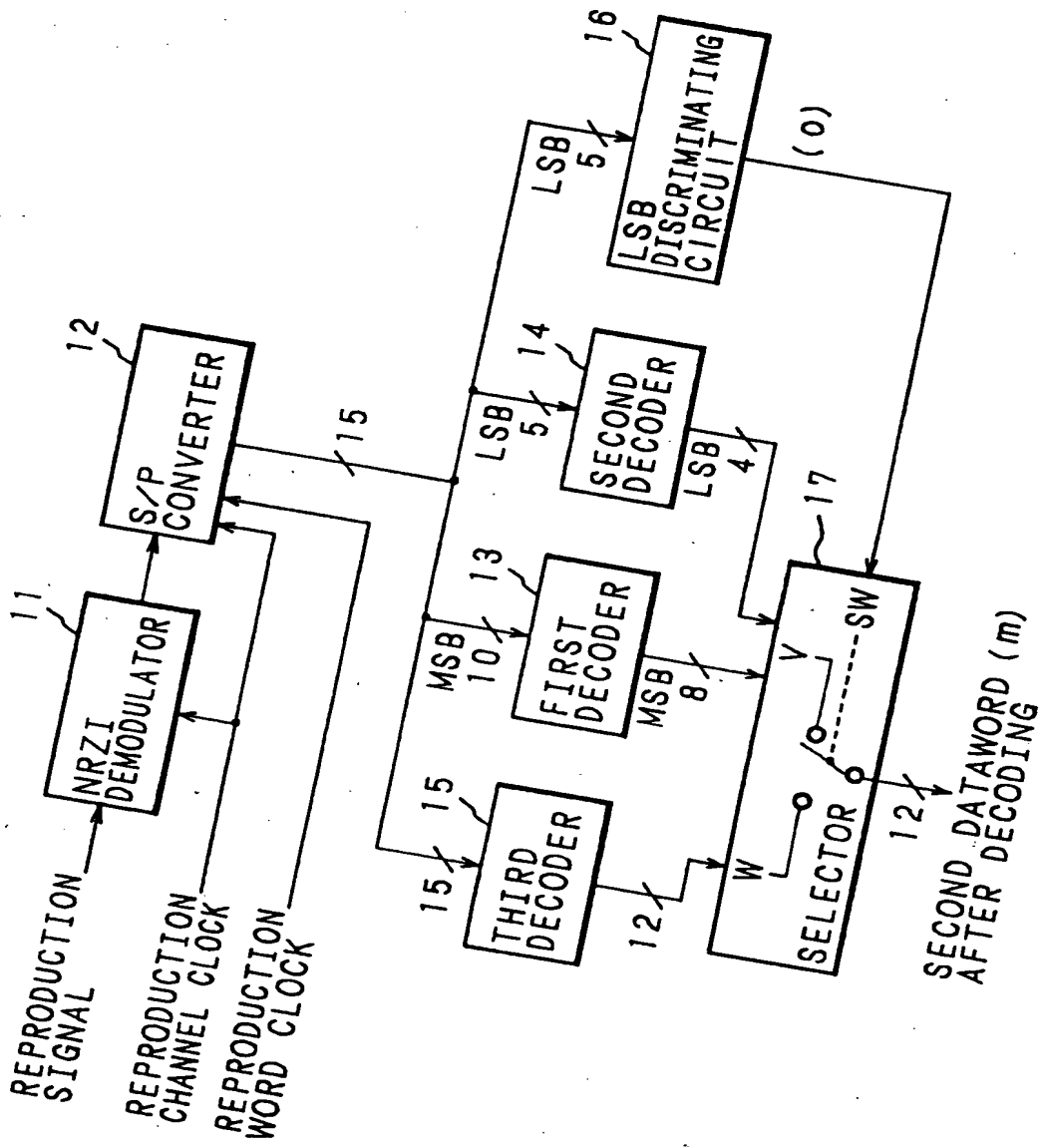


Fig. 32





F i g . 3 3

n2 GROUP	m2 DATA															
	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
1	07	09	0A	0B	0D	0E	0F	12	15	17	19	1A	1B	1D	1E	1F
2	01		03		05		06									
3											11		13			16
4		11		13		16		11	13	16		11		13	16	

Fig. 34(a)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00	4E7B	4E7E	CEA1	4EA3	4EA6	CEC2	4EC5	CEC7	4ECA	CECD	4ECF	CECE	4EDB	4ED6	CEE1	4EFF
01	4EF6	4F43	4F46	4F85	4F87	4F8A	4F8D	4F8F	4F99	4F9B	4F94	4F9C	4FB3	4FB6	CEE1	4FFE
02	4FE6	00B9	00B8	00BE	00D3	00D5	00E2	00E4	00E7	00E9	00EA	00ED	00F9	00FB	00F8	00F7
03	008A	008D	008F	008E	00D1	00D6	00E0	00E5	00E8	00EA	00EB	00ED	00F9	00FB	00F8	00F7
04	5113	5116	5118	5115	5127	512A	512E	512F	5139	513B	5138	513E	514B	514E	5152	5151
05	5157	515D	5158	515F	5169	516A	516E	516F	5175	5177	5178	517E	517F	5191	5193	5196
06	51A2	51A5	51A7	51A1	51AD	51AE	51B9	51B1	51BE	51C9	51CB	51CE	517F	5191	5193	5196
07	51D1	51D5	51E9	51E1	51EE	51F2	51F5	51F7	51FA	51FC	51FD	51CE	517F	5191	5193	5196
08	2475	24AD	24D5	24F9	2495	24B5	24E2	2471	2473	2466	246A	2489	2428	2426	2425	2457
09	229A	229D	229F	229E	22AB	22AE	22B2	22B5	22B7	22BA	22BD	22BF	2281	2292	229D	2297
0A	22E5	22E7	22EA	22ED	22E9	22F2	22F4	22FE	2231	2235	2237	223D	2235	2237	223A	223D
0B	232F	232D	232B	232E	2349	234B	234F	2352	2353	2357	235A	235D	235F	2369	236B	236E
0C	3372	3375	3377	337A	337D	337F	3391	3393	3395	3396	339A	339D	339A	339D	339F	339E
0D	33B8	33B5	33C9	33C8	33CE	33D2	33D5	33D7	33DA	33D5	33D8	33DF	33EA	33ED	33FA	33FB
0E	33F7	33F5	33FD	33FF	3343	3346	3348	3347	334A	334D	3348	334F	3349	334E	334F	334E
0F	44BB	44E1	44E3	44E6	4450	445B	445E	4451	4455	4451	4457	445A	4451	445D	445B	445E

Fig. 34(b)

Q=0

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
10	D532	5535	D537	553A	D53D	553F	D551	5553	5556	D562	5565	D567	556A	D56D	556F	D579
11	D557	557E	D5A1	555A	D5A6	5542	D55C	55C7	555CA	D5CD	55CF	D5D9	55DB	D5DE	55F1	D5F3
12	D55F	5561	D613	D616	5562	D625	5562	D62A	5562	D62F	5563	D63B	D63E	5564	D649	D564E
13	D665	5565	D66A	5565	D66A	5565	D669	D66B	5566	D667	5567	D669	D66A	5567	D67D	D6691
14	D669	5569	D66A	556A	D66A	556A	D66E	556A	556B	D66F	556B	D669	D66C	556C	D67D	D66D5
15	D66D	556D	D66A	556A	D66A	556A	D66E	556A	556B	D66F	556B	D669	D66C	556C	D67D	D66D5
16	D742	5574	D745	D74A	5574	D74F	5574	D74B	5575	D75E	5577	D776	5578	D78B	5578E	D792
17	D795	5579	D79A	5579	D79F	5579	D79B	557A	557B	D7B5	5577	D776	5578	D78B	5578E	D792
18	D7D6	557E	D7E5	D7E7	557E	D7ED	557E	D7F9	557F	D7FB	5577	D776	5578	D78B	5578E	D792
19	D91E	5593	D933	5593	D936	5593	D93A	559C	559C	D95A	559A	D90A	559A	D90D	559A	D91B
1A	D92B	55A2	D933	55A3	D936	55A3	D93A	55A3	55A5	D95A	55A5	D90A	559A	D90D	559A	D91B
1B	D96F	55A7	D995	55A7	D99A	55A3	D93A	55A3	55A5	D95A	55A5	D90A	559A	D90D	559A	D91B
1C	D9F1	55AF	D9F3	55B4	D9F6	55B8	D987	55B8	55B8	D98A	55B8	D98A	55B8	D98A	55B8	D98A
1D	D9F1	55BE	D9F3	55BE	D9F6	55B8	D987	55B8	55B8	D98A	55B8	D98A	55B8	D98A	55B8	D98A
1E	D9F1	55BE	D9F3	55BE	D9F6	55B8	D987	55B8	55B8	D98A	55B8	D98A	55B8	D98A	55B8	D98A
1F	D9F1	55BE	D9F3	55BE	D9F6	55B8	D987	55B8	55B8	D98A	55B8	D98A	55B8	D98A	55B8	D98A

Fig. 35(a)

Q=0

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
20	DCDA	5CDD	DD4A	CCDF	DD4A	5CEB	DD4A	5CEB	DD4A	5CEB	DD4A	5CEB	DD4A	5CEB	DD4A	5CEB
21	5D45	DD4A	5D45	DD4A	5D45	DD4A	5D45	DD4A	5D45	DD4A	5D45	DD4A	5D45	DD4A	5D45	DD4A
22	5D97	DD4A	5D97	DD4A	5D97	DD4A	5D97	DD4A	5D97	DD4A	5D97	DD4A	5D97	DD4A	5D97	DD4A
23	DE25	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
24	DE97	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
25	DE1D	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
26	DE67	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
27	DEFD	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
28	EB16	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
29	EB2B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
2A	EB6F	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
2B	EB9F	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
2C	EA4B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
2D	EA7F	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
2E	EA23	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B
2F	EB23	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B	DE9B

Fig. 35(b)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
30	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
31	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
32	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
33	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
34	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
35	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
36	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
37	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
38	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
39	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
3A	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
3B	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
3C	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
3D	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A
3E	EBBD16	6C47	EC9A	55F2	6ED7	7A6D	3E7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A	6D7A

Fig. 36(a)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
40	F21A	721D	F21F	F229	722B	722E	F232	7235	F237	723A	F23D	723F	F251	F253	7256	F262
41	F226	F227	F226A	F22D	7226F	7229F	722B	7227E	F22A	722A3	F22A6	F22C2	F22C5	722C7	722CA	F22CD
42	F22C	F22D9	722B3	F22DE	F226F	7229F	722B	7227E	F22A	722A3	F22A6	F22C2	F22C5	722C7	722CA	F22CD
43	F239	F2381	723B3	F23B6	F23E1	723E3	7236F	72343	F2346	72385	F2387	F238A	F238D	7238F	72399	F239B
44	F244	F244B	7244E	F2452	F2455	72457	7246F	72442	F2445	72427	F242A	F242D	F242F	72439	7243B	F243E
45	F247	F247D	7247F	F2493	F2496	72497	724A	7245D	F244A	72469	F246B	F246E	F2472	72475	72477	F247A
46	F24C	F24D2	724D5	F2493	F2496	72497	724A	7245D	F244A	72469	F246B	F246E	F2472	72475	72477	F247A
47	F252	F2523	72526	F2542	F2545	72547	7255F	7254D	F254A	72559	F255B	F255E	F255F	72573	72576	F2589
48	F258	F258E	72583	F2595	F2597	72597	725A	7259D	F259A	725AB	F25AE	F25B2	F25B5	725B7	725BA	F25BD
49	F25B	F25D1	725D3	F2595	F2597	72597	725A	7259D	F259A	725AB	F25AE	F25B2	F25B5	725B7	725BA	F25BD
4A	F268	F268A	7268D	F2687	F2699	72697	726A	7269D	F269A	726B6	F26E1	F26E3	F26E6	72699	726A6	F2685
4B	F271	F2715	7271A	F271A	F271D	7271F	7276	7272E	F2723	727B2	F2735	F2737	F273A	7273D	7273F	F2751
4C	F275	F2756	72752	F2765	F2767	7276A	727D	7276F	F2776	727B2	F2735	F2737	F273A	7273D	7273F	F2751
4D	F27C	F27CA	727CD	F27CF	F27D9	727DB	727E	727F1	F2776	727B2	F2735	F2737	F273A	7273D	7273F	F2751
4E	F285	F285B	7285E	F2871	F2873	72876	7288	7288E	F288E	72892	F2895	F2897	F289A	7289D	7289F	F28A9
4F	F28A	F28AE	728A2	F28B5	F28B7	728BA	728B	728BF	F28D1	728D3	F28D6	F28E2	F28E5	728E7	728EA	F28ED

Fig. 36(b)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
50	F8EE	F78F	F8FB	F8FE	F911	F791	F791	F922	F792	F925	F792	F92A	F792	F92F	F793	F793
51	F794	F94B	F94E	F795	F955	F795	F95A	F795	F95D	F795	F95F	F796	F96E	F797	F977	F797
52	F797	F97F	F79D	F97A	F79A	F97D	F79A	F97F	F79A	F97E	F79A	F97F	F79B	F97B	F79C	F79C
53	F79C	F9D2	F79D	F9D7	F79D	F9DD	F79D	F9D7	F79D	F9EB	F79D	F9E9	F79F	F9F7	F79F	F79F
54	FA21	F7A2	FA26	FA42	FA45	FA47	FA4A	FA4D	FA4F	FA59	FA5B	FA5E	FA71	FA73	FA76	FA89
55	FA8B	FA8E	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
56	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
57	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
58	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
59	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
5A	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
5B	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
5C	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
5D	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
5E	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89
5F	FA8B	FAD1	FA92	FA95	FA97	FA9A	FA9D	FA9F	FAA9	FAA9	FAAE	FA5E	FA7A	FA73	FA76	FA89

Fig. 37(a)

 $Q' = 0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
60	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
61	7EC9	FECE	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
62	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
63	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
64	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
65	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
66	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
67	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
68	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
69	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
6A	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
6B	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
6C	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
6D	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
6E	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE
6F	7E77	FE7A	7E7D	FE7F	FE91	7E93	7E96	FEA2	7EA5	FEA7	7EAA	FEAD	7EAF	FEB9	7EBB	7EBE



Fig. 37(b)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
70	0B91	8B93	8B96	0BA2	8BA5	0BA7	8BAA	0BAD	8BAF	0BB5	8BB8	0BBB	8BBE	0BC9	8BCB	0BCE	8BD2
71	0BD5	8BD7	0BDA	8BDD	0BDF	0BE9	8BE8	0BEE	0BFB	0BBF	8BBF	0BBF	8BBF	0BFD	8BFF	0BFF	8B46
72	0C85	8C87	0C8A	8CBD	0C8D	0C99	8C98	0C9E	0C9F	0CB3	8CB6	0CBF	8CBF	0CE3	8CE6	0CE8	7C0B
73	0D0E	8D12	0D15	8D17	0D1A	0D1D	8D1F	0D29	8D2B	0D2E	8D28	0D32	8D35	0D37	8D3A	0D3D	73F2
74	0D51	8D53	0D56	8D58	0D61	0D6D	8D6A	0D6D	8D6F	0D73	8D76	0D78	8D7E	0D7E	8D81	0D83	74C2
75	0DC5	8DC7	0DCA	8DCD	0DCF	0DD9	8DD8	0DDE	8DDE	0DE3	8DE6	0DE8	8DEE	0DEA	8DE1	0DE5	755F
76	0E27	8E2A	0E2D	8E2F	0E39	0E3B	8E3E	0E49	8E4B	0E4E	8E48	0E52	8E55	0E5A	8E51	0E55	765F
77	0E69	8E6B	0E6E	8E72	0E75	0E78	8E7A	0E7D	8E7F	0E8E	8E80	0E8E	8E8E	0E9A	8E91	0E95	77AA
78	0EAD	8EAF	0EAF	8EB8	0EBE	0EC9	8ECB	0ECE	8ECF	0ED5	8ED7	0EDF	8EDF	0EDF	8EDF	0EDF	78AB
79	0F59	8F5B	0F5E	8F71	0F73	0F78	8F78	0F8B	8F8F	0F8F	8F8F	0F92	8F92	0F9A	8F9D	0F9F	79A9
7A	0F59	8F5B	0F5E	8F71	0F73	0F78	8F78	0F8B	8F8F	0F8F	8F8F	0F92	8F92	0F9A	8F9D	0F9F	7A99
7B	0F59	8F5B	0F5E	8F71	0F73	0F78	8F78	0F8B	8F8F	0F8F	8F8F	0F92	8F92	0F9A	8F9D	0F9F	7B99
7C	0F59	8F5B	0F5E	8F71	0F73	0F78	8F78	0F8B	8F8F	0F8F	8F8F	0F92	8F92	0F9A	8F9D	0F9F	7C99
7D	10E3	8E31	0E31	8E31	0E31	0E31	8E31	0E31	8E31	0E31	8E31	0E31	8E31	0E31	8E31	0E31	7D99
7E	1371	83A0	013D	113F	0915	1153	8915	0151	8915	0151	8915	0151	8915	0151	8915	0151	7E99
7F	1A11	81A3	01A6	11C2	01C5	01C7	81C7	01C7	81C7	01C7	81C7	01C7	81C7	01C7	81C7	01C7	7F99

Fig. 38(a)

Q=0

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
80	9213	9216	1222	0225	1227	922A	122D	022F	1239	923B	923E	9249	124B	124E	9252	1255
81	9257	125A	925D	125F	1269	926B	126E	1272	1275	9278	927A	127D	127F	1291	9293	1296
82	92A2	12AD	92AE	12AF	12AD	92AE	12B0	12B1	12BE	92C9	92CB	92CE	12D2	12D5	92D7	12DA
83	12D4	92D7	12E9	12EB	12EE	92F1	12F5	12F7	12FA	92FD	92FF	9301	1303	1305	9307	130A
84	9334	133A	933D	133F	1349	934B	134E	1350	1353	9361	936D	136B	136E	1369	9371	1374
85	939A	139D	939F	13A9	13AB	93B1	13B5	13B7	13BA	93C8	93CA	13CB	13CE	13D3	93D5	13D8
86	13E5	93E7	13EE	93ED	13EF	93F1	13FB	13FE	1402	9423	9426	1442	1445	1447	944A	144D
87	944F	1459	945D	145E	1471	9473	1476	1489	148B	948E	9492	1495	1497	149A	949D	149F
88	14A9	94AB	14AF	14B2	14B5	94B7	14BA	14D9	14DF	94D1	94D3	14D6	14E2	14E5	94E7	14EA
89	14ED	94EF	14F9	14FB	14FE	9501	1503	1505	1508	9521	9527	152A	152D	152F	9539	153B
8A	153E	9549	1548	954F	1551	9555	1557	155A	155D	9560	9569	156B	156E	1569	9571	1577
8B	157A	957D	157F	1589	158A	9585	158D	158F	159A	9590	9595	1598	159E	1599	9599	159C
8C	15CB	95CE	15D2	15D5	15D7	95DA	15DD	15DF	15A9	95A0	95A1	15AF	15B8	15BB	95BF	15C9
8D	95FF	1562	1568	156E	1564	9565	1567	156A	156D	956A	9565	1568	156E	1569	9573	1576
8E	1689	9688	968E	169E	1695	9697	169A	169D	169F	96A9	96AB	16AE	16B2	16B5	96B7	16BA
8F	96BD	16B4	96D1	16D3	16D6	96E2	16E5	16E6	16EA	96ED	96EF	16F9	16FB	16FE	9711	1713

Fig. 38(b)

Q=0

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
90	716	722	725	727	72A	72D	72E	72F	739	73B	73E	749	74B	752	755	757
91	75A	75D	75F	769	76B	76E	772	775	777	779	77A	77D	77F	793	796	7A2
92	7A5	7A7	7AA	7AD	7AF	7B9	7BB	7BE	7C9	7CB	7CE	7D2	7D5	7D7	7DA	7DD
93	7DF	7E9	7EB	7EE	7F2	7F5	7F7	7FA	7FD	7FF	784	78A	788	787	78A	78D
94	788	789	78B	789	78B	78B	78B	78E	78E	78E	78E	78E	78E	78E	78E	78E
95	791	791	791	791	791	791	791	791	791	791	791	791	791	791	791	791
96	796	796	796	796	796	796	796	796	796	796	796	796	796	796	796	796
97	799	799	799	799	799	799	799	799	799	799	799	799	799	799	799	799
98	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3	7A3
99	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7	7A7
9A	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5	7B5
9B	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9	7B9
9C	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7
9D	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7	7B7
9E	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2	7C2
9F	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8	7C8

Fig. 39(a)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
A0	1CBFF	9CD11	CD31	CD69	9CE21	1CE59	9CE71	CEA9	9CED1	1CE9D	9CF91	1CFB1	1CFE1	1D119	1D139	1D16
A1	1D229	9D251	1D279	9D2A1	1D2D9	9D2F1	1D399	9D3B9	9D3E9	1D499	9D4B1	1D4E9	9D521	1D559	9D571	1D5A5
A2	9DA71	1DA99	9DA11	1DA69	9DAF9	1DBB1	9DBE1	1DC99	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
A3	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
A4	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
A5	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
A6	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
A7	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
A8	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
A9	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
AA	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
AB	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
AC	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
AD	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
AE	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF
AF	9DE49	1DE79	9DE11	1DE69	9DEF9	1DBF7	9DBA7	1DCE9	9DCB9	1DCE9	9DD79	1DD91	9DD41	1DD59	9DDA1	1DDF

Fig. 39(b)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
B0	2439A	43BA	43BA	449E	244B	244E	A452	2455	A457	245A	A45D	245F	2469	A46B	A46E	2472
B1	A475	2477	A477	A47D	A47F	A491	A493	2496	A4A2	A4A5	A4A7	24AA	A4AD	A4AF	A4B9	24BB
B2	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
B3	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
B4	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
B5	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
B6	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
B7	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
B8	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
B9	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
BA	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
BB	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
BC	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
BD	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
BE	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7
BF	2424	FAA4	4C8A	4CEA	24D2	4D5A	24D7	24DA	24DD	A4DFA	4E9D	24EB	24EE	4F5B	24F5	A4F7

Fig. 40(a)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
C0	A9FA	29FD	A9FF	AA21	2A23	2A26	AA42	2A45	AA47	2A4A	AA4D	2A4F	AA55	2A5B	2A5E	AA71
C1	2A73	2A76	2A89	AA8B	AA8E	2A92	AA95	2A97	AA9A	2A9D	AA9F	AA99	2AA8	2AAE	2AAB	2AB5
C2	2AB1	2AB4	2AB7	2ABF	2AD1	2AD3	2AD0	2AAE	2AB2	2AE7	2AEA	2AAD	2AED	2AF9	2AFB	2AFE
C3	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
C4	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
C5	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
C6	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
C7	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
C8	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
C9	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
CA	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
CB	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
CC	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
CD	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
CE	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3
CF	2AB1	2AB3	2AB5	2ABD	2ABF	2AB9	2AB6	2ABE	2AB8	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3	2AB3

Fig. 40(b)

Q=0

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
D0	2FB22	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D1	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D2	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D3	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D4	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D5	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D6	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D7	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D8	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
D9	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
DA	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
DB	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
DC	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
DD	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
DE	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9
DF	2FFB3	2FFB3	2FB77	2FB87	AFBDA	2FB8D	AFD12	2FD33	2FD6A	AFE22	2FE5A	AFE77	2FEA7	AFEDD	2FEF8	AFFF9

Fig. 41(a)

Q=0

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
1	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
2	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
3	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
4	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
5	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
6	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
7	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
8	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
9	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
A	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
B	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
C	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
D	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
E	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4
F	57FB	59D1	59D3	59D5	59D7	59D9	59DA	59DB	59DD	59DE	59DF	59E0	59E1	59E2	59E3	59E4



Fig. 41(b)

 $Q=0$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
F0	3A	F8	21	38	23	47	8B	4A	BB	4D	3B	59	38	5E	BB	71
F1	3B	89	BB	22	33	97	8B	9D	BB	9F	BB	5B	8B	23	BB	73
F2	BB	8D	33	BB	66	BE	23	BE	73	BE	33	3B	8B	22	33	BB
F3	BC	26	3C	47	BC	4A	3C	59	BC	5B	3C	71	BC	76	3C	8E
F4	BC	92	3C	9A	BC	9D	3C	AB	BC	9E	3C	53	BC	BA	3C	D1
F5	BC	CD	3B	CE	7B	CE	DA	BC	CE	FB	8C	FE	BD	13	D1	3D
F6	BD	27	3D	2A	BD	3D	3B	3D	49	BD	4E	3D	57	BD	5A	3D
F7	BD	69	3D	6B	3D	77	3D	7A	BD	7D	3D	93	BD	DA	53	DA
F8	3D	AD	BB	3D	BB	DC	93	DC	BB	DD	23	DD	73	DD	3D	DE
F9	BB	DE	3D	FE	7B	FE	8D	FE	21	3E	26	8E	42	3E	4A	BE
FA	BB	DE	59	3E	71	3E	8B	3E	8F	3E	92	3E	9A	3E	9D	BE
FB	3E	AB	3E	85	BE	73	BE	8D	BE	ED	13	EE	23	EE	73	EE
FC	3E	AF	3E	FE	3F	1B	FE	16	3F	25	3F	2A	3F	2B	3F	2B
FD	3F	7D	3F	93	3F	55	3F	5A	3F	5F	3F	6B	3F	6B	3F	6B
FE	3F	7D	3F	93	3F	55	3F	5A	3F	5F	3F	6B	3F	6B	3F	6B
FF	3F	CE	3F	FD	3F	DA	3F	DA	3F	DA	3F	DA	3F	DA	3F	DA

Fig. 42(a)

Q=1

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00	CC	B5	4CB	7CC	BA4	CD1	CC	D3	CC	D6	4CD	64	CE	22	4D	68
01	CC	FE	CD1	14D	13A	4D2	5F	CD	25	4D	25	4D	25	4D	25	4D
02	4D	52	CD	5A	54	DA	7C	DE	4A	9C	DE	4A	9C	DE	4A	9C
03	CD	93	74	DD	4A	55	CD	5A	54	DA	7C	DE	4A	9C	DE	4A
04	CD	93	74	DD	4A	55	CD	5A	54	DA	7C	DE	4A	9C	DE	4A
05	CE	49	4E	97	4C	EE	2C	EE	2C	EE	2C	EE	2C	EE	2C	EE
06	CE	49	4E	97	4C	EE	2C	EE	2C	EE	2C	EE	2C	EE	2C	EE
07	CE	49	4E	97	4C	EE	2C	EE	2C	EE	2C	EE	2C	EE	2C	EE
08	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF
09	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF
0A	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF
0B	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF
0C	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF
0D	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF
0E	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF
0F	CF	26	4A	CF	6B	FF	22	4C	FF	6B	FF	22	4C	FF	6B	FF

Fig. 42(b)

Q=1

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
10	D425	5427	D42A	542D	D42F	5439	D43B	D43E	D449	544B	544E	D452	5455	D457	545A	D45D
11	545F	5469	D46B	D46E	5472	D475	5477	D47A	547D	D47F	D491	5493	5498	D4A2	54A5	D4A7
12	54A4	D4AD	54AE	D4B5	54BB	54BE	D4C9	D4CB	D4CE	54D2	D4D5	54D7	D4DA	54DD	D4DF	D4E9
13	54E4	EEBF	D4F2	54F5	D4FF	54FA	D4FD	54FF	D4CE	5521	5523	D542	5545	D547	554A	D54D
14	5549	D55B	555E	555B	D55D	5557	556A	5568	D58B	558F	5592	D595	5597	D59A	559D	D59F
15	D55D	555B	555E	555B	555D	5557	556A	5568	D58B	558F	5592	D595	5597	D59A	559D	D59F
16	D55D	555B	555E	555B	555D	5557	556A	5568	D58B	558F	5592	D595	5597	D59A	559D	D59F
17	556B	556E	D56E	556B	556E	556A	D56B	D56E	556B	556E	556A	D56B	D56E	556B	556E	D56B
18	5572	5573	5577	5573	557A	557D	557F	557E	5573	5576	5571	D57A	557D	557F	557E	D57B
19	D57D	557B	557E	557B	557D	5577	557A	557E	5573	5576	5571	D57A	557D	557F	557E	D57B
1A	558D	558F	5584	558A	5587	558D	558F	558E	5583	5586	5581	D58A	558D	558F	558E	D58B
1B	558D	558F	5584	558A	5587	558D	558F	558E	5583	5586	5581	D58A	558D	558F	558E	D58B
1C	558D	558F	5584	558A	5587	558D	558F	558E	5583	5586	5581	D58A	558D	558F	558E	D58B
1D	5592	5595	D592	559B	D59F	5597	559A	5593	D595	5598	5591	D59A	559D	559F	559A	D59A
1E	D59A	559D	D59A	559B	D59F	5597	559A	5593	D595	5598	5591	D59A	559D	559F	559A	D59A
1F	D59A	559D	D59A	559B	D59F	5597	559A	5593	D595	5598	5591	D59A	559D	559F	559A	D59A

Fig. 43(a)

 $Q=1$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
20	9EBB	9EEB	59F2	09F5	59F7	09FA	59FD	09FF	DA21	5A23	5A26	DA42	5A45	DA47	5A4A	DA4D
21	5A4F	DA59	5A5B	5A5E	DA71	5A73	5A76	5A89	DA8B	DA8E	5A92	DA95	5A97	DA9A	5A9D	DA9F
22	DA95	5AAB	5A9F	DA25	5AB5	DA77	5ABD	DA8D	5ABF	DAD1	5AD3	5ADD	DAE2	5AE5	DAE7	5AEA
23	DAED	5AEF	DA49	5AFB	5AFE	DB13	DB16	DB5A	DB5B	DB25	5B27	DB2A	DB2D	5B2F	DB39	DB3B
24	DB3E	5B7D	DB4B	5B4E	5B52	DB57	DB5A	5B5B	DBA7	5BA8	DBAD	5BAF	DBB6	5BB7	DBB9	5BBB
25	DB7A	5BCE	5B7F	DB91	5B93	5B96	DBA5	5BAF	DBA7	5BA8	DBAD	5BAF	DBB6	5BB7	DBB9	5BBB
26	DBCB	5C43	5C4B	DBD5	5C47	DBDA	5CDD	DBDF	DBE9	5BE8	DBEE	5CBF	DBF2	5CB6	DBF5	5CB8
27	5BCE	5D09	5D0B	DC85	5D12	DC8A	5D17	DC1A	DC9D	5C9B	DC9E	5D2B	DC3E	5D3C	DC3F	5DCE
28	55D3	AD03	5D03	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE
29	55DA	35DA	5D3C	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE
2A	55DA	35DA	5D3C	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE
2B	55DA	35DA	5D3C	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE
2C	55DA	35DA	5D3C	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE
2D	55DA	35DA	5D3C	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE
2E	55DA	35DA	5D3C	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE
2F	55DA	35DA	5D3C	DD51	5D53	DD56	5DD1	DD65	DD67	5D6A	DD6D	5D95	DD79	5D7D	DD7E	5DCE

Fig. 43(b)

Q=1

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
30	5F9D	DF9F	DFED	5FAE	5FAE	DFB2	5FB5	DF67	5F6A	DFBD	5FBF	DFD1	5FD3	5FD6	DFE2	5FE5
31	FF75	FE86	FE86	FF75	FF75	FFB5	FFB5	FE84	684B	684E	FE85	6855	FE85	685A	FE85	685F
32	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
33	68AD	68AF	68AF	68AF	68AF	68C7	68C7	687D	687D	687D	6893	6899	6899	6899	6899	6899
34	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
35	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
36	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
37	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
38	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
39	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
3A	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
3B	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
3C	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
3D	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
3E	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899
3F	689E	689E	689E	689E	689E	689E	689E	687D	687D	687D	6893	6899	6899	6899	6899	6899

Fig. 44(a)

Q=1

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
40	709A	F09D	709F	70A9	F0AB	F0AE	70B2	F0B5	70B7	F0BA	70BD	F0BF	70D1	F0D3	F0D6	70E2
41	F0E5	70E7	F0EA	70ED	F0EF	70F9	F0FD	F0FE	F111	7113	7116	F122	F125	F127	F12A	F12D
42	712F	F139	713B	713E	F149	F14B	F14E	F152	F155	7157	F16A	F16D	F16F	7169	716B	716E
43	F172	F175	7177	717A	F17D	F17F	7191	F193	F196	71A2	F1A5	71A7	F1A9	71AD	71AE	71B9
44	F1BB	F1BE	F1C9	71CB	F1CE	F1D2	71D5	F1D7	71DA	F1DD	71DF	71E9	F1EB	71EE	71F2	71F5
45	71F7	F1FA	71FD	F1FF	F221	F223	7226	F229	7245	F247	724A	F24D	7249	7259	725B	725E
46	F271	7273	7276	F289	F28B	F28E	7292	F295	7297	F29A	729D	F29F	72A9	72AB	72AE	72B5
47	72B5	F2B7	72BA	F2BD	F2B8	F2BD	72D3	F2D6	72E2	F2E5	72E9	F2EB	72ED	72EF	72F9	72FB
48	F2FE	7311	F313	F316	F228	F22D	7326	F329	7332	F335	7339	F33C	733E	7349	734B	734E
49	F339	7335	F33A	F335	F228	F22D	7336	F339	7332	F335	7339	F33C	733E	7349	734B	734E
4A	733D	7339	F33A	F335	F228	F22D	7336	F339	7332	F335	7339	F33C	733E	7349	734B	734E
4B	733D	7339	F33A	F335	F228	F22D	7336	F339	7332	F335	7339	F33C	733E	7349	734B	734E
4C	F487	748A	F4BD	748D	F499	F49B	7499	F4B1	74B3	F4B6	74BA	F4BE	74C3	74C6	74D2	74D5
4D	7512	F515	7517	F51A	F51D	F51F	7529	F532	7535	F538	754E	F551	7556	F559	756B	756E
4E	F553	7556	F55C	755F	F567	F56A	756D	F56F	7579	F582	7585	F588	759A	759D	75A6	75A9
4F	F55C	755C	F55D	755F	F55D	755D	755D	F55F	755F	755F	7561	F561	7561	7561	7561	7561

Fig. 44(b)

Q=1

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
50	F62A	762D	F62F	7639	F63B	F63E	F649	764B	764E	F652	7655	F657	765A	F65D	765F	7669
51	F66B	F66E	7672	F675	7677	F67A	767D	F67F	F691	7693	7696	F6A2	76A5	F6A7	76AA	F6AD
52	76AF	F6B9	76BB	76BE	76C9	F6CB	F6CE	76D2	F6D5	76D7	F6DA	76DD	F6DF	F6E9	76EB	76EE
53	F6F2	76F5	F6F7	76FA	F6FD	76FF	F721	7723	7726	F742	7745	F747	774A	F74D	774F	7759
54	775B	775E	F771	7773	7776	7789	F78B	F78E	7792	F795	7797	F79A	779D	F79F	77A9	77AB
55	77AE	F7B2	77B5	F7B7	77BA	F7BD	77BF	F7D1	77D3	77D6	F7E2	77E5	F7E7	77EA	F7ED	77EF
56	F7F9	77FB	77FE	7886	790A	F90D	790F	F915	791B	791E	F931	7933	F936	F961	7963	7966
57	79C3	79C6	7A12	FA15	7A17	FA1A	7A1D	FA1F	FA29	7A2B	FA2E	FA32	7A35	FA37	7A3A	FA3D
58	7A3F	FA51	7A53	7A56	FA62	7A65	FA67	7A6A	FA6D	7A6F	FA79	7A7B	FA7E	FAA1	7AA3	7AA6
59	FAC2	7AC5	FAC7	7ACA	FACD	7ACE	FAD9	7ADB	FADE	FAF1	7AF3	7AF6	7B43	7B46	7B85	7B87
5A	7BBA	FB8D	7B8F	FB99	7B9B	FB9E	FBBI	7BB3	7BB6	FBBI	7BE3	7BE6	7C22	7C25	7C27	7C2A
5B	7C2D	FC2F	7C39	FC3B	FC3E	FC49	7C4B	FC4E	FC52	7C55	FC57	7C5A	FC5D	7C5F	7C69	7C6B
5C	FC6E	7C72	FC75	7C77	FC7A	7C7D	FC7F	FC91	7C93	FC96	FCAC	7CA5	FCAD	7CAA	FCAD	7CAF
5D	FCB9	7CB8	FCBE	7CC9	FCCE	7CCE	FCD2	FCDD	7CD7	FCDA	7CDD	FCDF	FC99	7CEB	7CEE	7CF2
5E	7CF5	FCF7	7CFA	FCFD	7CFF	FFD2	7D23	7D26	FFD4	7D45	FFD4	7D4A	FFD4	7D4F	7D59	7D5B
5F	7D5E	FFD7	7D73	7D76	7D89	FFD8	7D8E	7D92	FFD9	7D95	FFD9	7D9A	FFD9	7D9F	7DAB	7DAE

F 1 8 . 4 5 ( a )

 $Q=1$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
60	FDB2	7DD7	7DBA	FDBA	FDBD	7DBB	FDD1	7DD3	7DD6	FDE2	7DE5	FDE7	7DEA	FDED	7DEE	FDF9
61	7DDB	7DDE	7E43	7E46	7E85	7E8E	7E8A	FE8D	7E8F	FE99	7E9B	FE9E	7EB1	7EB3	7EB6	FE91
62	7EE3	7EE6	7F09	7F0B	7F5E	7F5F	7F51	7F62	7F6A	7F6D	7F6F	7F6E	7F2B	7F2E	7F2F	7F35
63	7FF3	7FFA	7FF3	7FFC	7FF5	7FF7	7FF5	7FF6	7FF1	7FFD	7FFA	7FFD	7FF6	7FF7	7FF7	7F7E
64	7FFA	7FA3	7FA6	7FFC	7FF5	7FF7	7FF5	7FF6	7FF1	7FFD	7FFA	7FFD	7FF6	7FF7	7FF7	7F84
65	8845	7084	884A	884D	884F	884E	885B	885E	8871	8873	8876	888D	888B	888E	8892	8895
66	8897	889A	889D	889F	889E	889D	888A	888B	8885	888B	888B	888D	888B	888D	888D	8896
67	88E2	88E5	88E7	88EA	88ED	888E	888A	888F	8891	8891	8891	8891	8892	8892	8892	889A
68	892D	892E	8939	893B	893E	894B	894B	894E	8955	8955	8957	895A	895A	895A	895A	896B
69	896E	8972	8975	8977	897A	897D	897D	897F	8983	8986	8989	8989	8989	8989	8989	8989
6A	8995	899B	899C	899F	899B	899C	899D	899D	899D	899D	899D	899D	899E	899E	899E	899F
6B	89F5	89F7	89FA	89FD	89FF	898A	898A	898A	898A	898A	898A	898A	898A	898A	898A	898A
6C	8A5E	8A71	8A73	8A76	8A89	8A8B	8A8E	8A92	8A95	8A97	8A9A	8A9A	8A9A	8A9A	8A9A	8A9A
6D	8AB2	8AB5	8AB7	8ABA	8ABD	8ABF	8AD1	8AD3	8AD6	8AD8	8AD9	8AD9	8AD9	8AD9	8AD9	8A99
6E	8AFB	8AFE	8B11	8B13	8B16	8B22	8B25	8B27	8B2A	8B2E	8B2E	8B2E	8B2E	8B2E	8B2E	8B2E
6F	8B4E	8B52	8B55	8B57	8B5A	8B5D	8B5F	8B69	8B6B	8B6E	8B6E	8B6E	8B6E	8B6E	8B6E	8B6E



Fig. 45(b)

Q=1

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
70	8B91	0B93	0B96	8BA2	0BA5	8BA7	0BA7	8BAD	0BAF	8BB9	0BBB	0BBE	0BC9	8BCB	8BCE	0BD2
71	8BD5	0BD7	8BDA	0BDD	8BDF	8BE9	0BE9	8BEB	8BF2	0BF5	8BF8	0BFA	0BF9	8BFF	0B43	0B46
72	8C58	0C5D	8C8A	0CB1	8C8F	8C90	0C9D	8CB0	8CB8	0CB3	8CB6	8CE1	0CE3	8CE6	0C09	8C0B
73	8D0E	0D05	8D1A	0D17	8D18	8D1D	0D19	8D29	8DB1	0DB2	8DB3	8DB7	8DA1	8DA3	8D3D	8D3F
74	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
75	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
76	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
77	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
78	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
79	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
7A	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
7B	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
7C	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
7D	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
7E	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F
7F	8D51	0D53	8D56	0D62	8D65	8D6D	0D6A	8D6D	8DBF	0DBF	8DBF	8DBF	8DA1	8DA3	8D3D	8D3F

Fig. 46(a)

 $Q=1$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
80	1312579	2A2D12	3A39D1	4A79D2	5A125D	6A126B	7A122A	8D122D	9F122F	0D122D	1A122A	2A122A	3B122B	4B122B	5E122E	6A122A
81	125792A	2D1234A	39D13E7	4A79D2	5A125D	6A126B	7A122A	8D122D	9F122F	0D122D	1A122A	2A122A	3B122B	4B122B	5E122E	6A122A
82	12A2D12	3A39D1	4A79D2	5A125D	6A126B	7A122A	8D122D	9F122F	0D122D	1A122A	2A122A	3B122B	4B122B	5E122E	6A122A	7A122A
83	13479A	2D1234A	39D13E7	4A79D2	5A125D	6A126B	7A122A	8D122D	9F122F	0D122D	1A122A	2A122A	3B122B	4B122B	5E122E	6A122A
84	13479A	2D1234A	39D13E7	4A79D2	5A125D	6A126B	7A122A	8D122D	9F122F	0D122D	1A122A	2A122A	3B122B	4B122B	5E122E	6A122A
85	13479A	2D1234A	39D13E7	4A79D2	5A125D	6A126B	7A122A	8D122D	9F122F	0D122D	1A122A	2A122A	3B122B	4B122B	5E122E	6A122A
86	13479A	2D1234A	39D13E7	4A79D2	5A125D	6A126B	7A122A	8D122D	9F122F	0D122D	1A122A	2A122A	3B122B	4B122B	5E122E	6A122A
87	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
88	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
89	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
8A	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
8B	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
8C	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
8D	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
8E	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E
8F	144A91	4A914E	5A914E	6A914E	7A914E	8A914E	9A914E	0A914E	1A914E	2A914E	3A914E	4A914E	5A914E	6A914E	7A914E	8A914E

Fig. 46(b)

 $Q=1$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
90	17169	7221	7259	7271	72A9	72D1	72F9	7339	73B1	73E1	7499	774B	774E	7529	7551	757
91	75A1	75D9	75F9	7691	76B1	76E9	7721	7759	7771	77A9	777D	77F1	7919	7933	7961	7A2
92	7A51	7A79	7AA1	7AD9	7AF1	7B99	7BB9	7BE9	7C91	7CB1	7CE9	7D21	7D59	7D71	7DA9	7DD
93	7DF1	7E99	7EB9	7EE1	7F29	7F51	7F79	7FA1	7FD9	7FF1	7843	78A6	7859	7887	78A9	78D
94	78F9	7899	78B1	78E9	78B1	78B3	78B6	78E1	78E3	78E6	7909	790B	790E	7912	7915	7917
95	791A	791D	791F	7929	792B	792E	7932	7935	7937	793A	793D	793F	7951	7953	7956	7962
96	7965	7967	796A	796D	796F	7979	797B	797E	799A	79A3	79A6	799C	7995	799C	799A	799D
97	79CF	799D	79DB	79DE	799F	79F3	79F6	79A1	79A1	79A1	79A1	79A2	79A2	79A2	79A2	79A2
98	79A3	79A3	79A3	79A4	79A4	79A4	79A5	79A5	79A5	79A5	79A5	79A5	79A5	79A5	79A5	79A5
99	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7
9A	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7
9B	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7
9C	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7
9D	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7
9E	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7
9F	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7	79A7

Fig. 47(a)

 $Q=1$ 

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
A0	9CBF	1CD1	9CD3	9CD6	1CE2	9CE5	1CE7	9CEA	1CED	9CEH	1CF9	9CFB	1D52	9D99	1D11	9D16
A1	9D22	1D25	9D27	1D2A	9D2D	1D2F	9D31	1D3B	9D3E	1D49	9D4C	1D4E	9D51	1D55	9D57	1D5A
A2	1D5D	9D5F	1D69	9D6B	1D6E	9D72	1D75	9D77	1D7A	9D7D	1D7F	9D81	1D8D	9D89	1D8D	9D8F
A3	1DA7	9DA9	1DAE	9DAF	1DB9	9DBB	1DBE	9DBF	1DCB	9DCE	1DCE	9DD1	1DD5	9DD9	1DDA	9DDA
A4	1DE9	9DEA	1DEE	9DEF	1DF9	9DFB	1DFE	9DF7	1DFF	9DE8	1DE9	9DE2	1DE4	9DE5	1DE7	9DE4
A5	9EE9	1EEA	9EE5	1EEB	9EEB	1EE7	9EEF	1EE3	9EE8	1EE9	9EE1	1EE2	9EE5	1EE7	9EE9	1EEA
A6	9EEA	1EEA	9EE5	1EEB	9EEB	1EE7	9EEF	1EE3	9EE8	1EE9	9EE1	1EE2	9EE5	1EE7	9EE9	1EEA
A7	9EEA	1EEA	9EE5	1EEB	9EEB	1EE7	9EEF	1EE3	9EE8	1EE9	9EE1	1EE2	9EE5	1EE7	9EE9	1EEA
A8	9F77	9F79	1F7D	9F7E	1F7F	9F83	1F86	9F87	1F8A	9F8D	1F8F	9F91	1F94	9F97	1F99	9F9D
A9	1F77	9F79	1F7D	9F7E	1F7F	9F83	1F86	9F87	1F8A	9F8D	1F8F	9F91	1F94	9F97	1F99	9F9D
AA	1FC7	9FC9	1FCF	9FD2	1FD5	9FD7	1FDA	9FD9	1FDF	9FDD	1FE1	9FE4	1FE6	9FE9	1FEB	9FEA
AB	9FC7	1FC9	9FCF	9FD2	1FD5	9FD7	1FDA	9FD9	1FDF	9FDD	1FE1	9FE4	1FE6	9FE9	1FEB	9FEA
AC	9A21	1A22	9A27	1A2A	9A2D	1A2F	9A31	1A3B	9A3E	1A49	9A4C	1A4E	9A51	1A55	9A57	1A5A
AD	9A21	1A22	9A27	1A2A	9A2D	1A2F	9A31	1A3B	9A3E	1A49	9A4C	1A4E	9A51	1A55	9A57	1A5A
AE	9A21	1A22	9A27	1A2A	9A2D	1A2F	9A31	1A3B	9A3E	1A49	9A4C	1A4E	9A51	1A55	9A57	1A5A
AF	9A38	1A39	9A3A	1A3B	9A3C	1A3D	9A3E	1A3F	9A3G	1A3H	9A3I	1A3J	9A3K	1A3L	9A3M	1A3N

Fig. 47(b)

$Q=1$

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
B0	439	243B	243E	2449	44B	44E	2452	4552	2457	45A	245D	45F	A469	246B	246E	A472
B1	475	A477	247A	447D	447F	A491	A493	A496	24A2	A4A	24A7	A4AA	24AE	A4AF	24B9	A4BB
B2	A475	A4C9	24C8	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
B3	A4FA	24FD	A4FF	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
B4	A573	A576	A589	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
B5	A5B7	A5BA	A589	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
B6	A643	A646	A685	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
B7	A709	A73B	270E	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
B8	273D	A73C	2751	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
B9	A7A6	27C2	A7C5	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
BA	A84A	A84D	284F	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
BB	A897	A89A	289B	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
BC	289E	A89D	A89F	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
BD	A935	A937	A93A	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
BE	A975	297A	297B	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7
BF	29BE	29C9	A9CB	24CE	44D2	24D5	A4D7	24DA	44DD	24DF	24E9	A4EB	24EE	A4F2	24F5	A4F7

Fig. 50

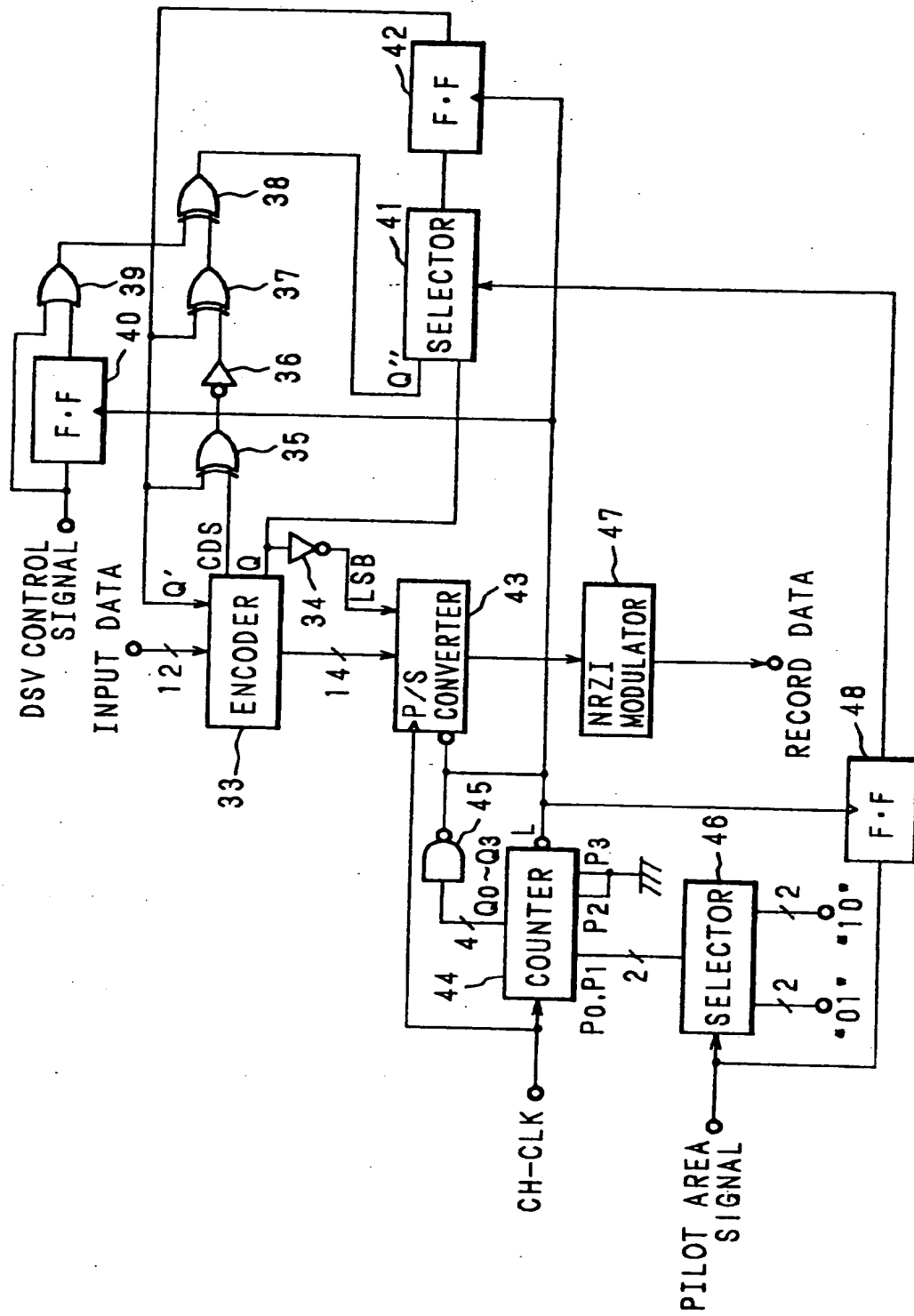


Fig. 51

DSV	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
DSV'	0	0	0	0	1	1	1	1	1	0	0	1	1	1	1	1	1
Q'	0	0	0	1	1	0	0	1	1	1	0	0	1	0	0	1	1
CDS	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	1
Q''	1	0	1	0	0	1	0	1	0	1	0	1	0	1	0	1	0

FIG. 52(A)

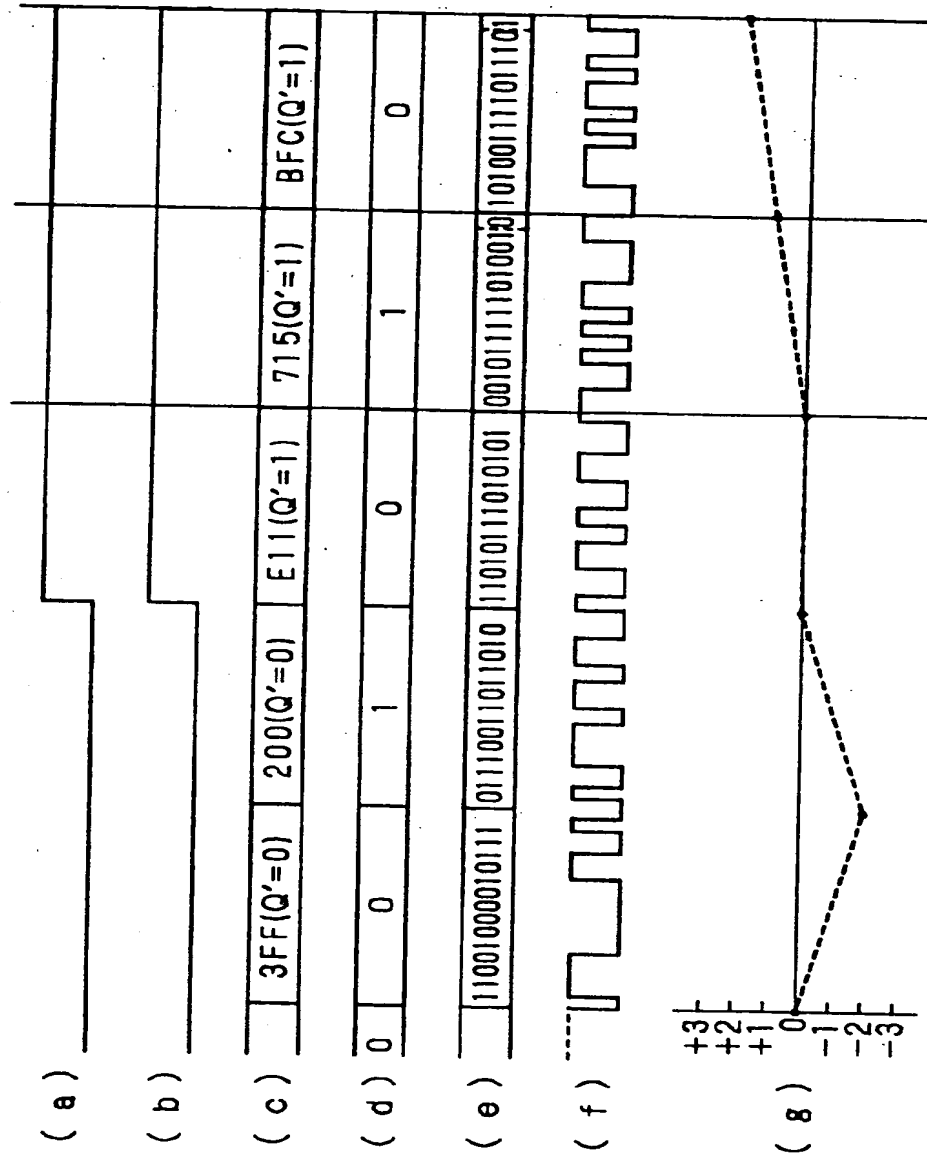
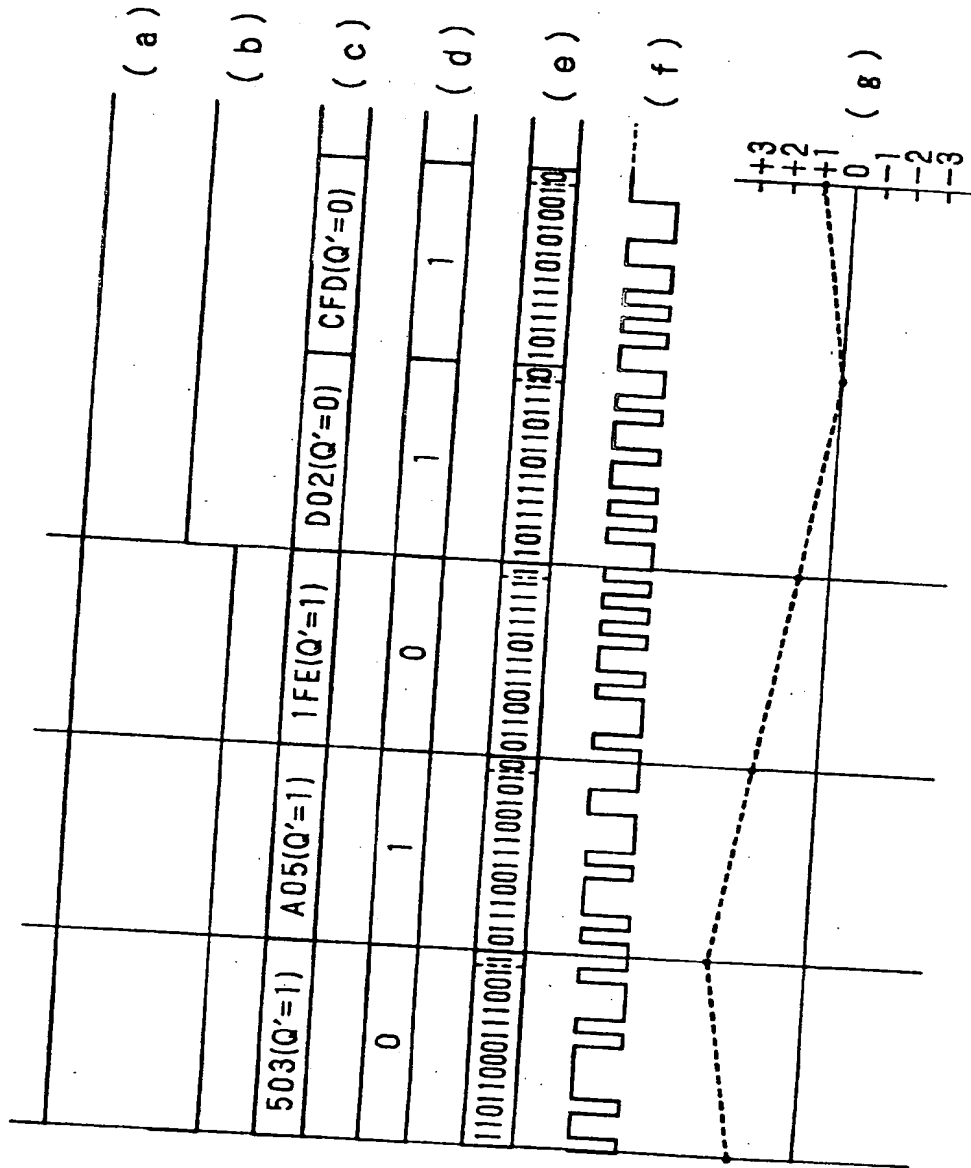




Fig. 52(B)



F 1 8. 53

MARGIN2	
SUB2	POST AMBLE3
	SUB DATA2
	PRE AMBLE3
	IBG2
MAIN	POST AMBLE2
	MAIN DATA
	PRE AMBLE2
SUB1	IBG1
	POST AMBLE1
	SUB DATA1
	PRE AMBLE1
MARGIN1	



⑪ Publication number : **0 557 130 A3**

⑫

## EUROPEAN PATENT APPLICATION

⑳ Application number : **93301251.0**

⑤ Int. Cl.<sup>5</sup> : **H03M 5/14, G11B 20/14, G11B 20/12**

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㉗ Applicant : **MITSUBISHI DENKI KABUSHIKI KAISHA**  
**2-3, Marunouchi 2-chome**  
**Chiyoda-ku**  
**Tokyo (JP)**

㉘ Inventor : **Ido, Kihel, c/o Mitsubishi Denki K.K.**  
**Denshi-shohinkaihatsu Kenkyusho,**  
**1, Babazusho**  
**Nagaokakyo-shi, Kyoto 617 (JP)**  
Inventor : **Yamada, Masako, c/o Mitsubishi Denki K.K.**  
**Denshi-shohinkaihatsu Kenkyusho,**  
**1, Babazusho**  
**Nagaokakyo-shi, Kyoto 617 (JP)**  
Inventor : **Kosaka, Hideaki, c/o Mitsubishi Denki K.K.**  
**Denshi-shohinkaihatsu Kenkyusho,**  
**1, Babazusho**  
**Nagaokakyo-shi, Kyoto 617 (JP)**  
Inventor : **Ohta, Masayuki, c/o Mitsubishi Denki Eng. K.K.**  
**Kyoto Jigyosho,**  
**1, Babazusho**  
**Nagaokakyo-shi, Kyoto 617 (JP)**

㉙ Representative : **Burke, Steven David et al**  
**R.G.C. Jenkins & Co.**  
**26 Caxton Street**  
**London SW1H 0RJ (GB)**

⑤④ Data conversion method and recording/reproducing apparatus using the same

⑤⑦ A data conversion method, wherein a sequence of first  $r$ -bit datawords is divided into groups of  $x$  bits where  $x$  is the least common multiple of  $r$  and  $m$ , an arbitrary first dataword selected from  $x/r$  groups of first datawords is divided into  $x/m$ , an  $m$ -bit second dataword is formed by appending  $r/(x/m)$ -bit data, obtained by dividing the first dataword into  $x/m$ , to the LSB or MSB side of one or other of the non-divided first datawords, and the word-converted  $m$ -bit second dataword is converted to an  $n$ -bit codeword ( $m < n$ ).

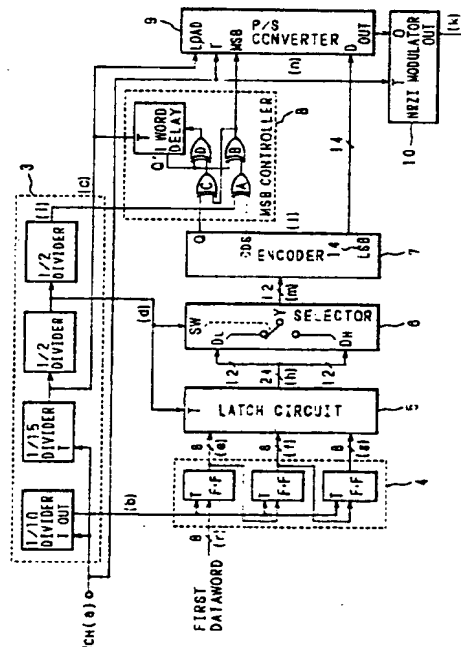


Fig. 27

## EUROPEAN SEARCH REPORT

**Application Number**  
**EP 93 30 1251**

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claims	CLASSIFICATION OF THE APPLICATION (Int. Cl. 5)
A	EP-A-0 405 885 (MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD) * claim 1; figure 1 *	1,5,11	H03M5/14 G11B20/14 G11B20/12
A	EP-A-0 178 589 (HITACHI LTD) * page 8, line 27 - page 9, line 11; claim 1; figure 7 *	1,5,11	
A	EP-A-0 338 781 (SONY CORPORATION) * column 2, line 34 - column 3, line 14; figure 5 *	1,5,11	
A	FR-A-2 551 277 (SONY CORPORATION) * page 18, line 20 - line 23; figure 4 *	1,5,11	
A	GB-A-2 111 805 (PIONEER ELECTRIC CORPORATION) * claim 1 *	1,5,11	
A	EP-A-0 426 033 (SONY CORPORATION) * claim 5; figures 1,2 *	1,4	
X	EP-A-0 250 049 (N.V. PHILIPS' GLOEILAMPENFABRIEKEN) * column 6, line 13 - column 7, line 21 * * column 11, line 49 - line 58 *	6	
A	EP-A-0 339 724 (N.V. PHILIPS' GLOEILAMPENFABRIEKEN) * page 4, line 56 - page 5, line 22 *	6,7	
A	EP-A-0 104 700 (N.V. PHILIPS' GLOEILAMPENFABRIEKEN) * claim 1 *	7,8	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 18 September 1995	Examiner Brunet, L
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document & : member of the same patent family, corresponding document	



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### CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

- ☐ All claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claims:
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

### LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet -B-

- ☒ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respects of which search fees have been paid, namely claims:
- ☐ None of the further search fees has been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:



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# EUROPEAN SEARCH REPORT

Application Number  
EP 93 30 1251

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. CL.5)
A	IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol.37, no.3, August 1991, NEW YORK, US pages 252 - 259, XP000263193 KEN ONISHI ET AL. 'An experimental home-use digital VCR with three dimensional DCT and superimposed error correction coding' * paragraph 5.2 *	8	
A	EP-A-0 321 314 (SHARP K. K.) * column 4, line 16 - line 27 *	9	
			TECHNICAL FIELDS SEARCHED (Int. CL.5)
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 18 September 1995	Examiner Brunet, L
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document & : member of the same patent family, corresponding document	

EPO FORM 503 (01.92) (P44-01)



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EP 93 30 1251 -B-

**LACK OF UNITY OF INVENTION**

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims 1-5,11: Data conversion method dividing first datawords and re-arranging them into second datawords
2. Claims 6-10 : Data conversion method with special control of the digital sums

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